

FIG. 3A

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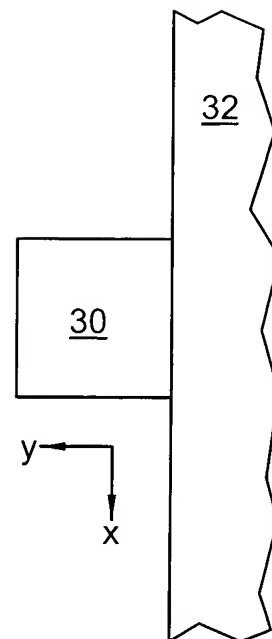
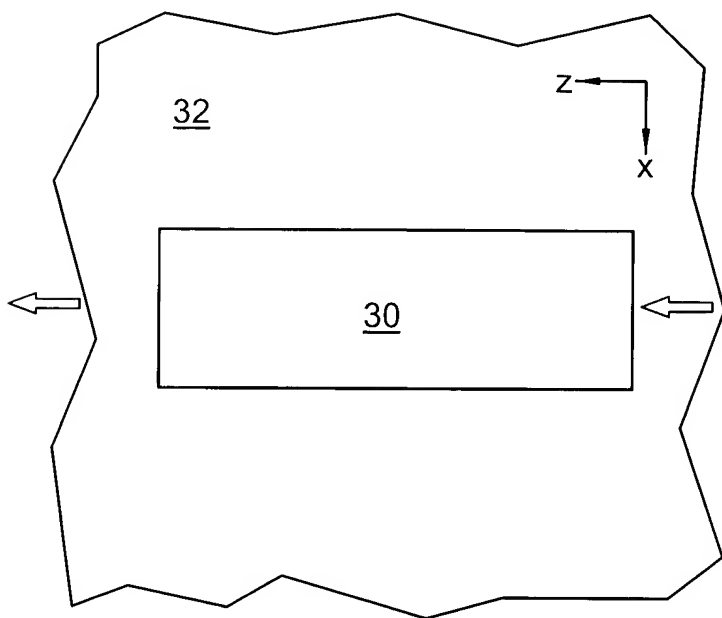


FIG. 3B

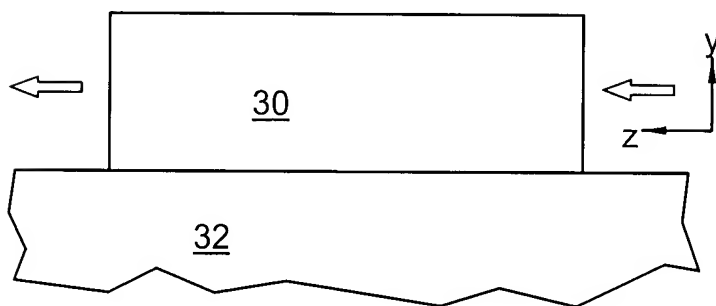


FIG. 3C

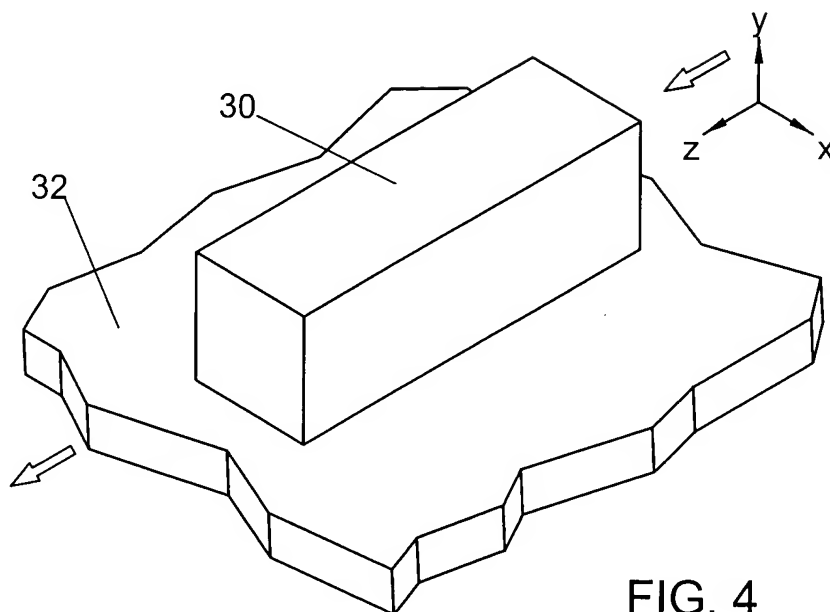


FIG. 4

FIG. 5A

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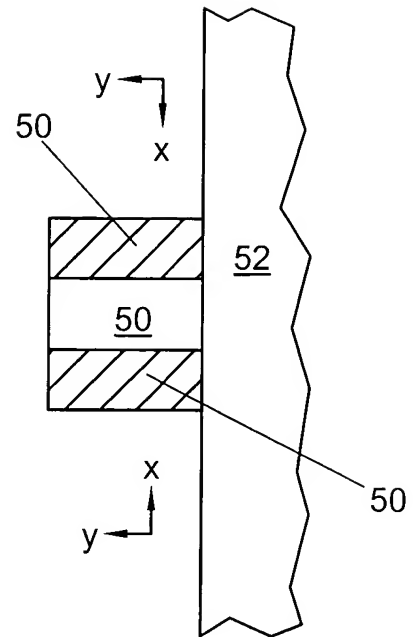
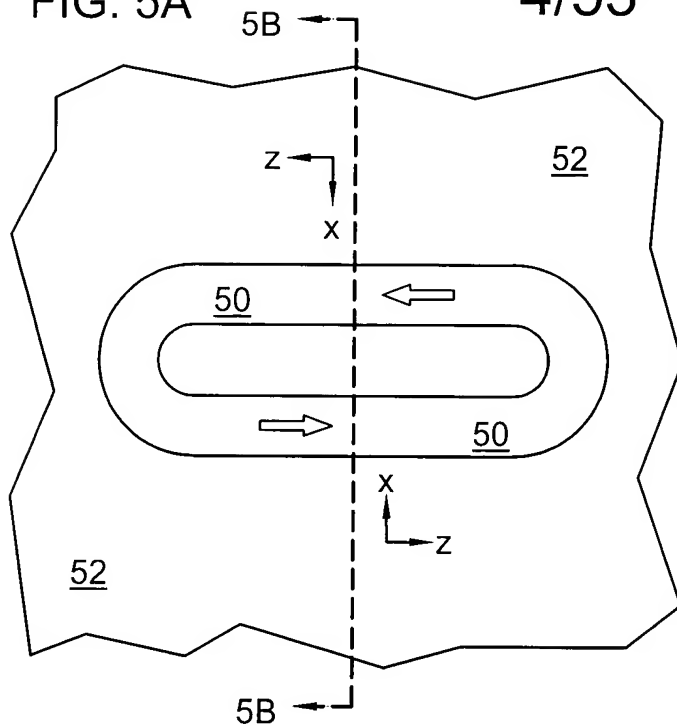


FIG. 5B

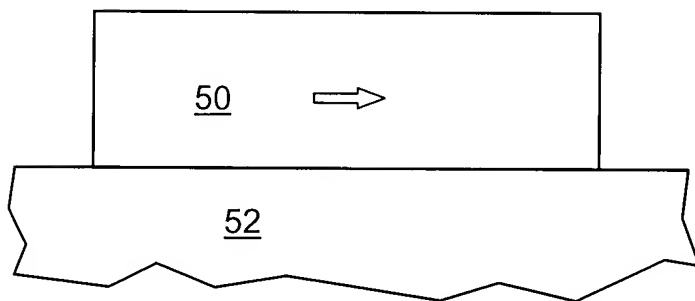


FIG. 5C

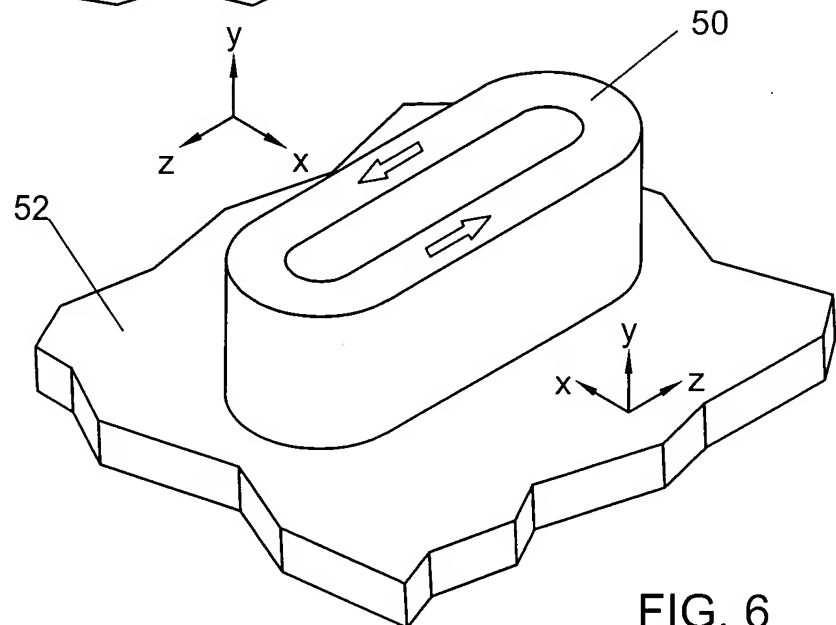


FIG. 6

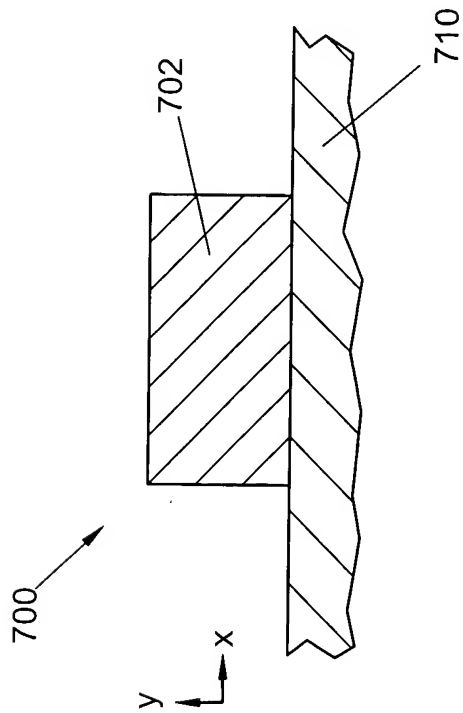


FIG. 7

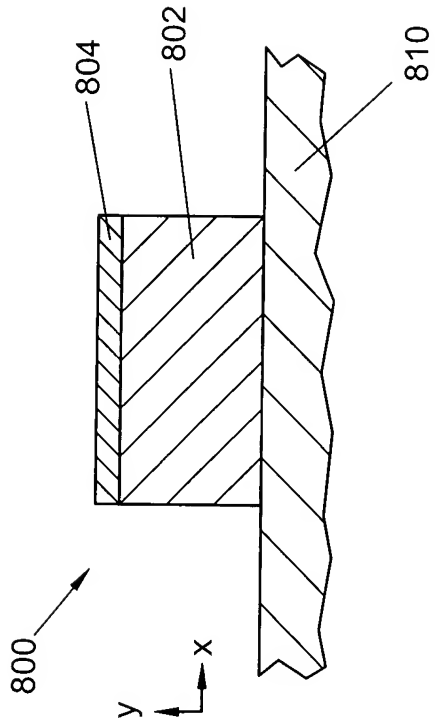


FIG. 8

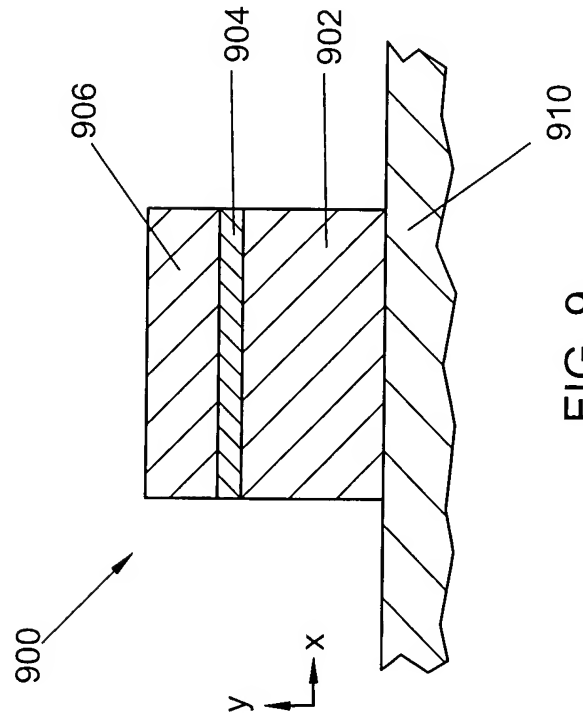


FIG. 9

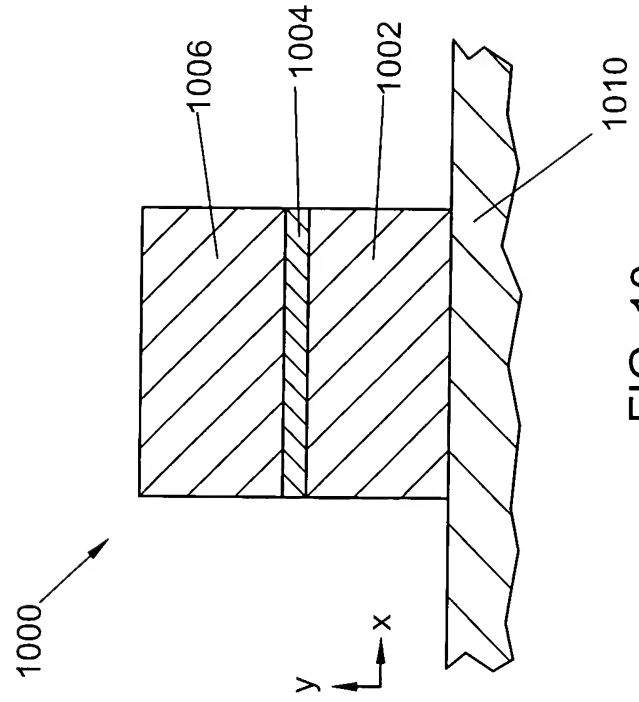


FIG. 10

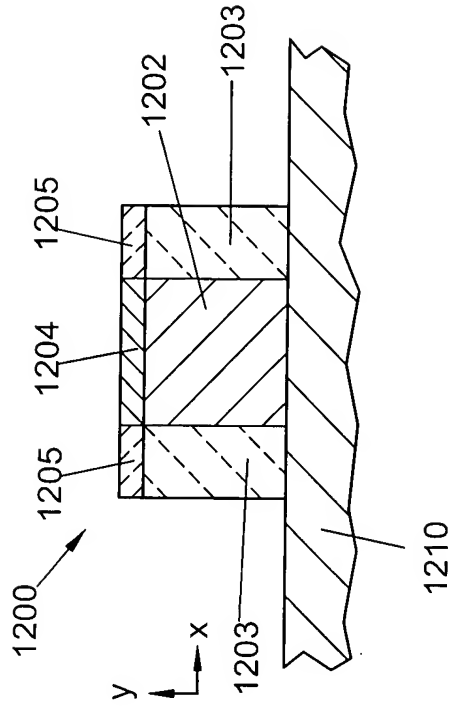


FIG. 11

FIG. 12

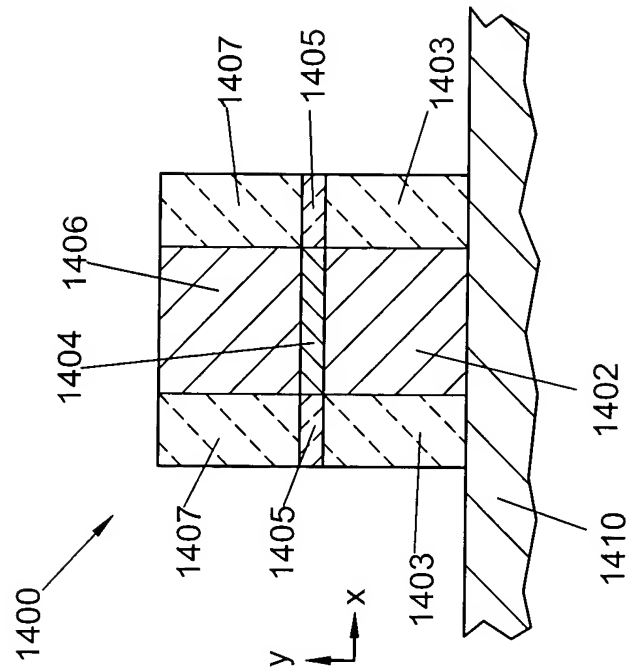


FIG. 13

FIG. 14

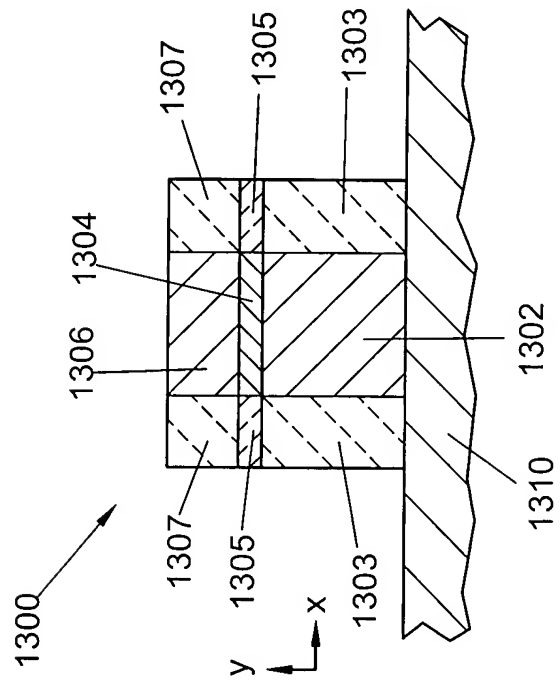


FIG. 14

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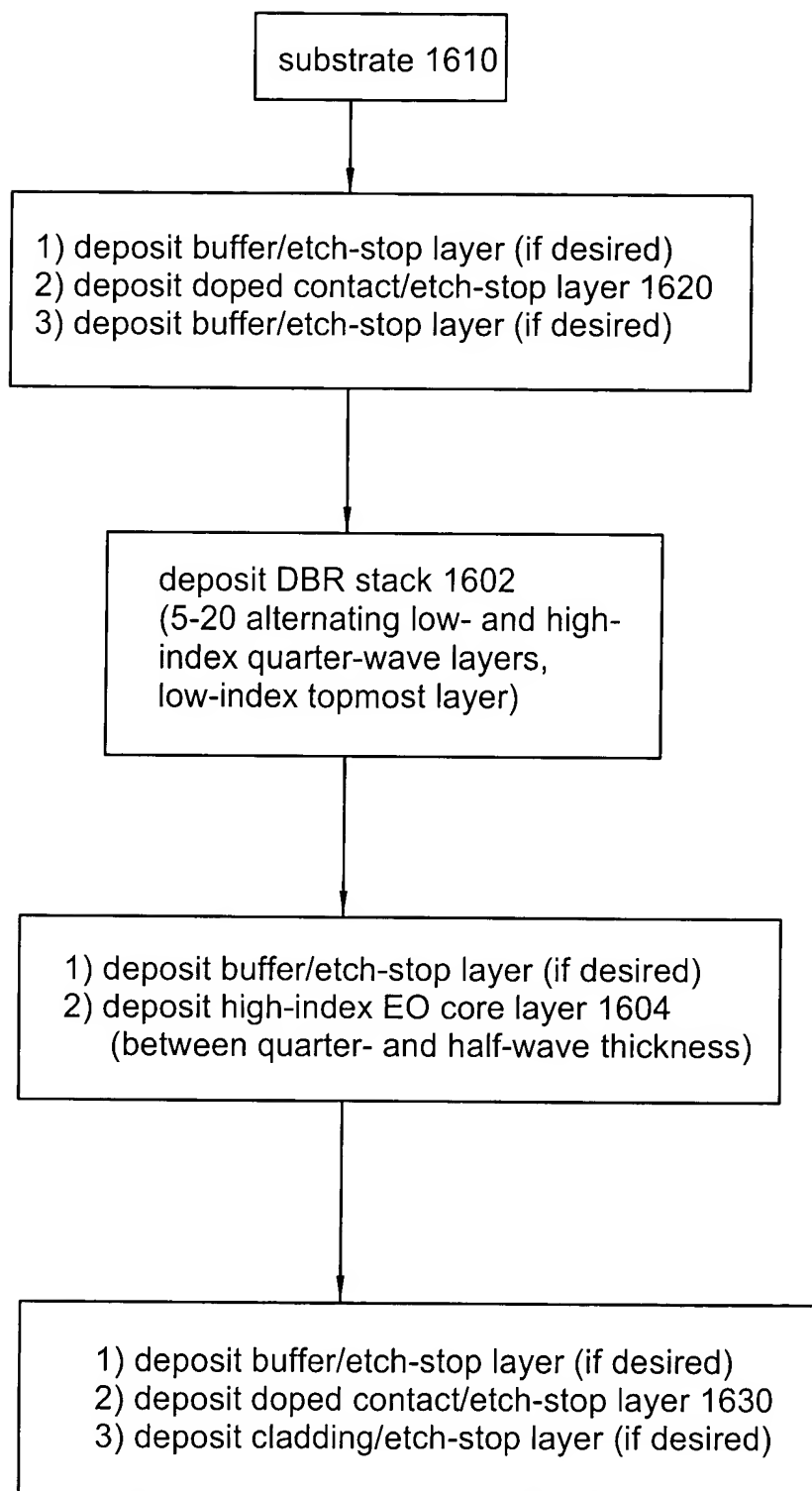


FIG. 15

FIG. 16 is a schematic diagram of a method for forming a multi-layered structure. The method includes the steps of: (a) providing a substrate 1610; (b) depositing a first layer 1620 on the substrate 1610; (c) depositing a second layer 1602 on the first layer 1620; (d) depositing a third layer 1604 on the second layer 1602; and (e) depositing a fourth layer 1630 on the third layer 1604. The resulting structure is a multi-layered structure 1610.

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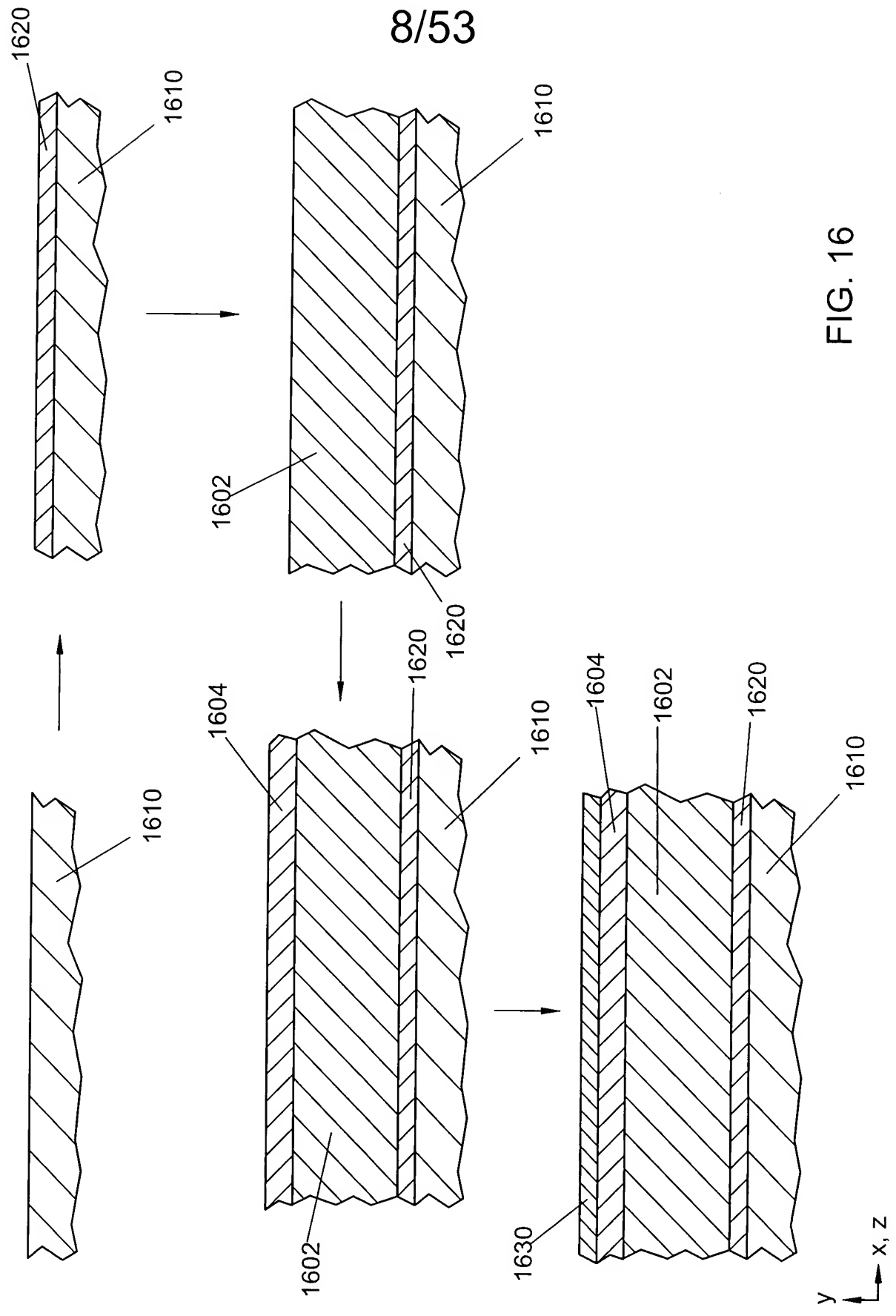


FIG. 16



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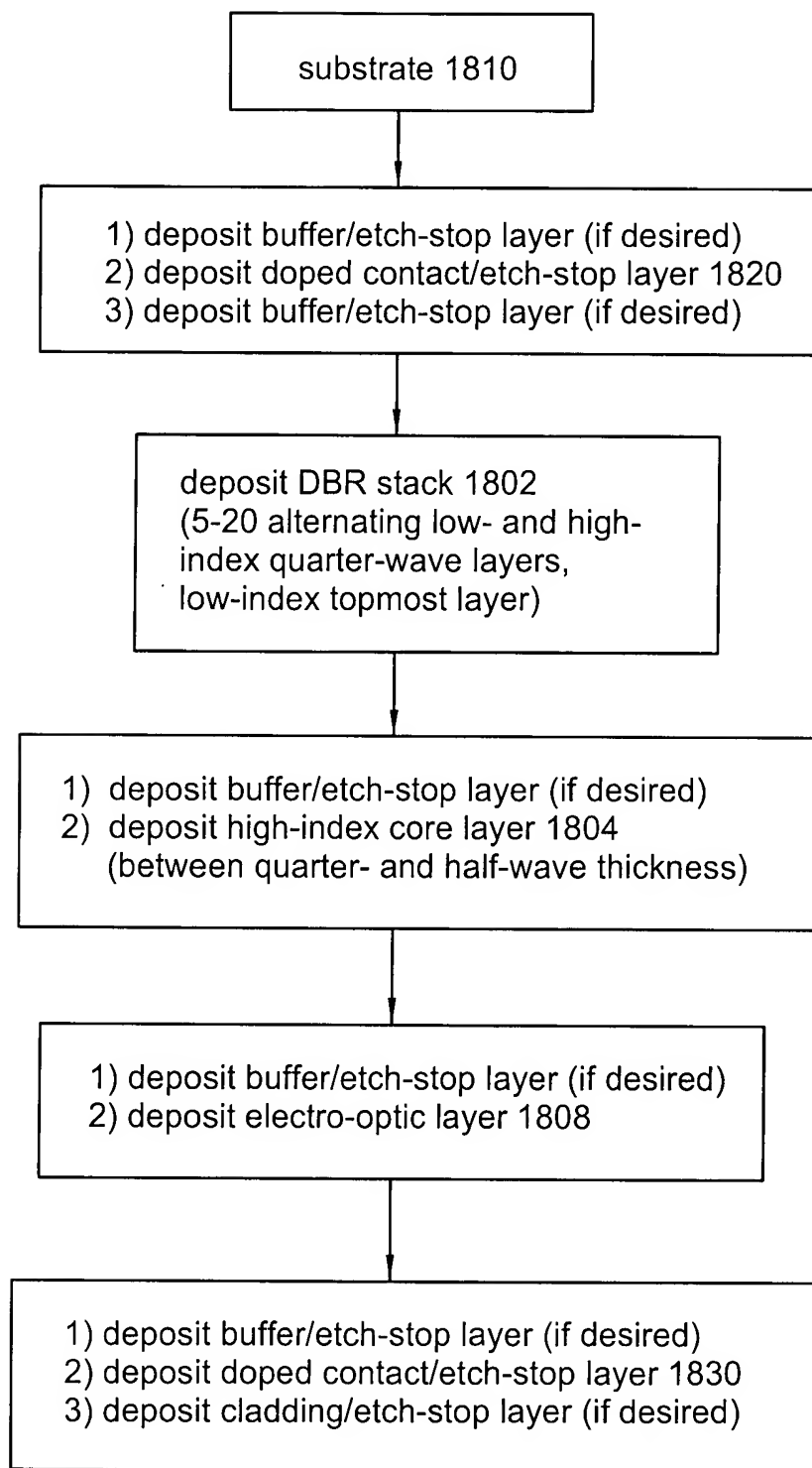


FIG. 17

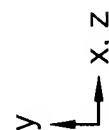


FIG. 18

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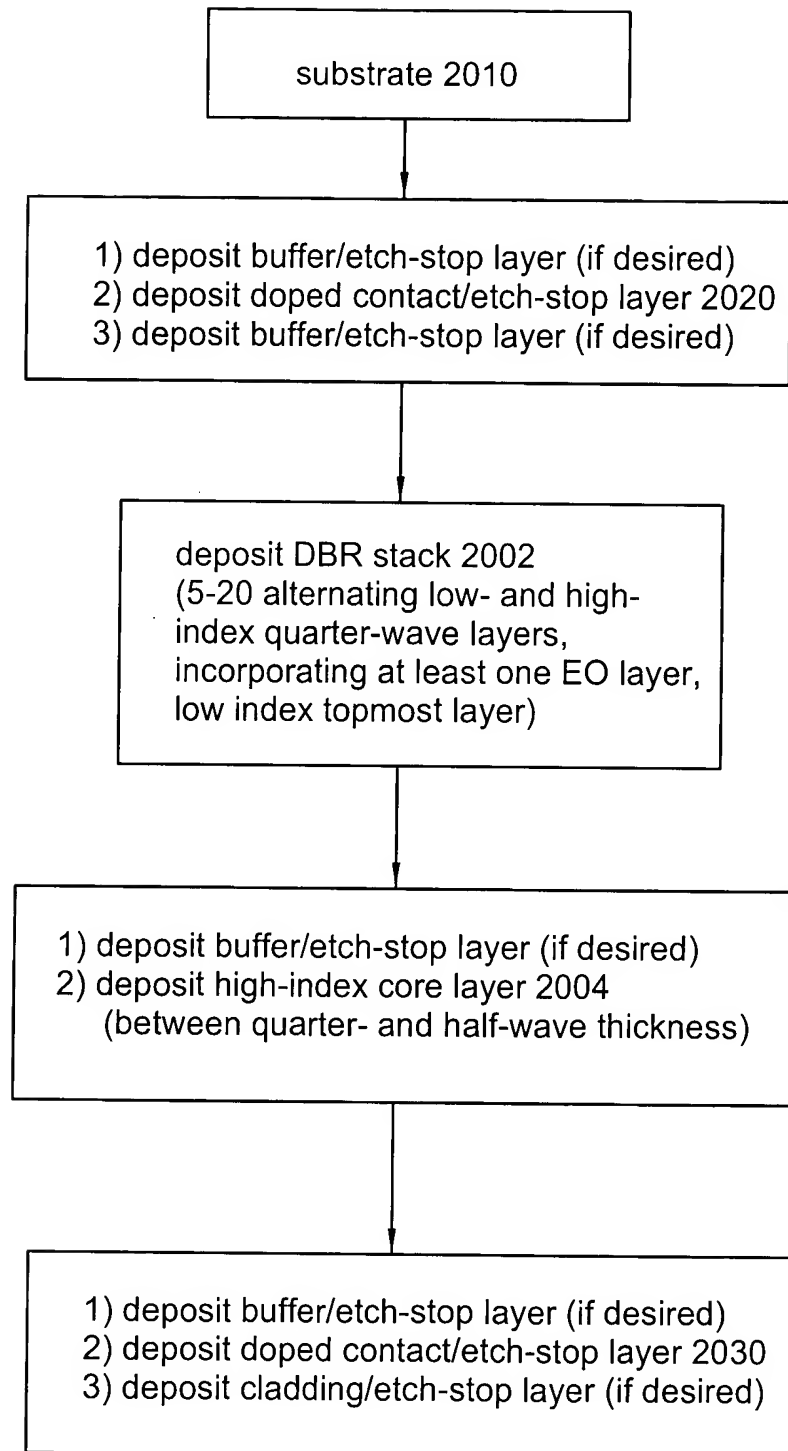


FIG. 19

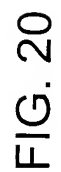


FIG. 20

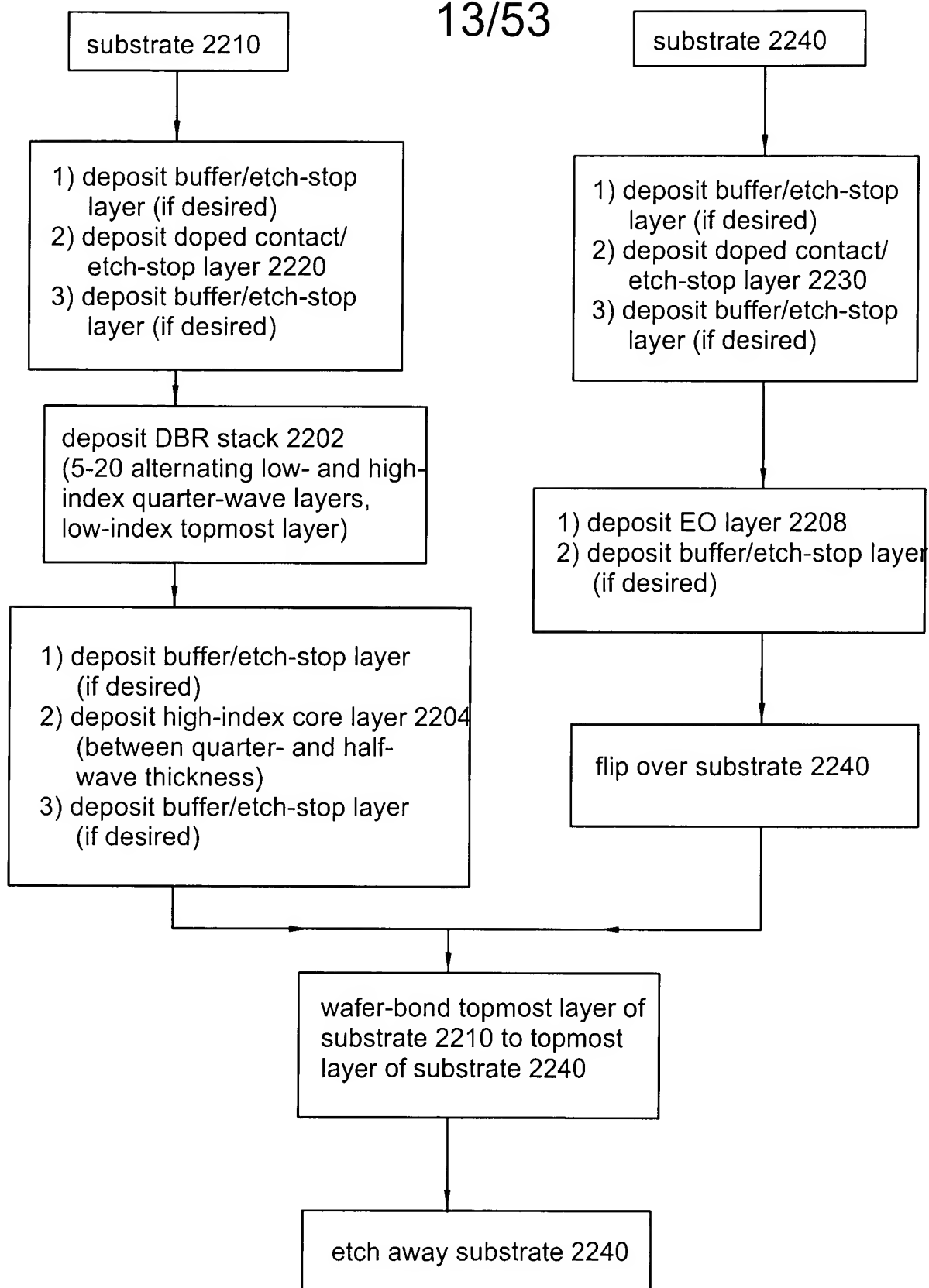


FIG. 21

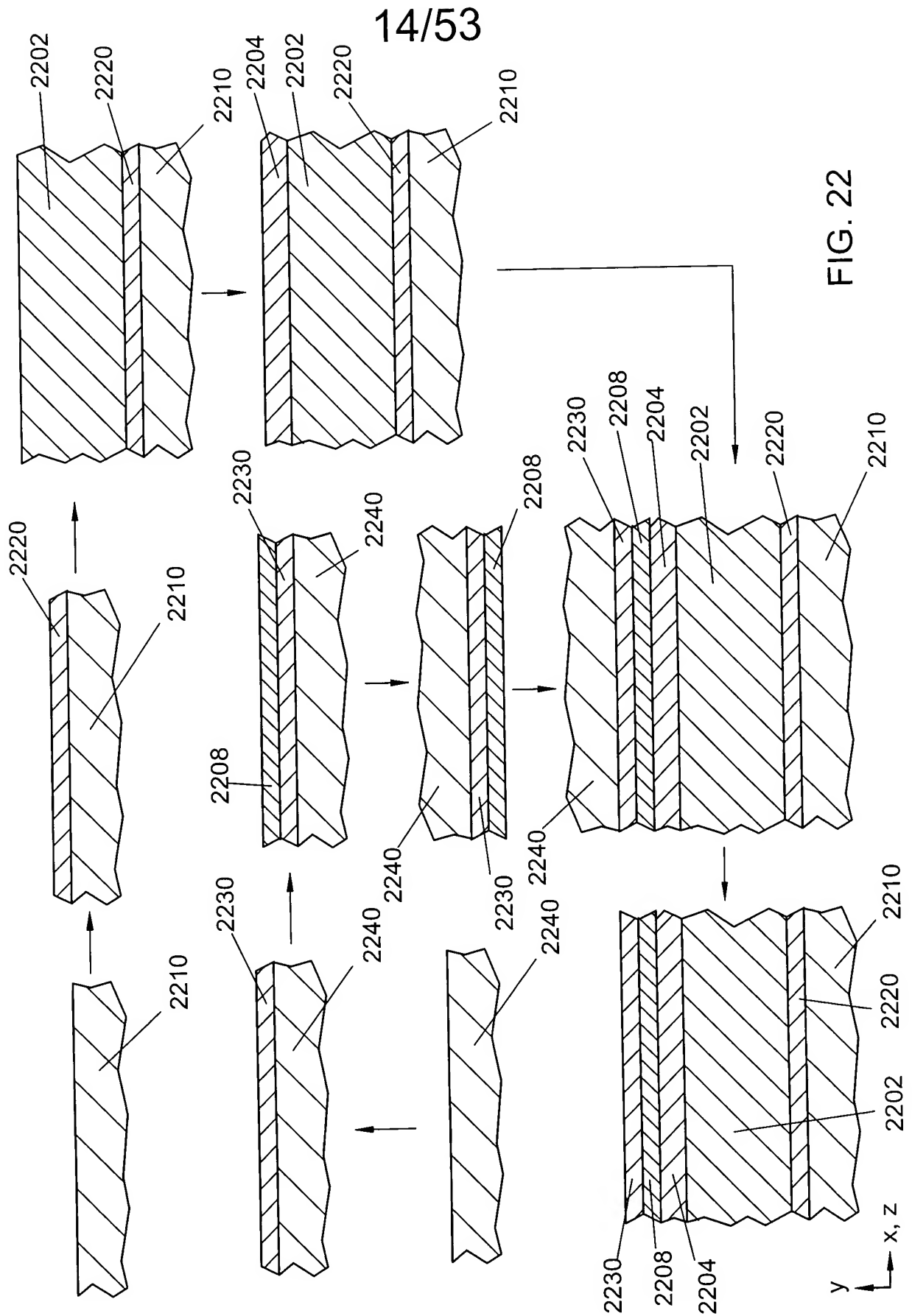


FIG. 22

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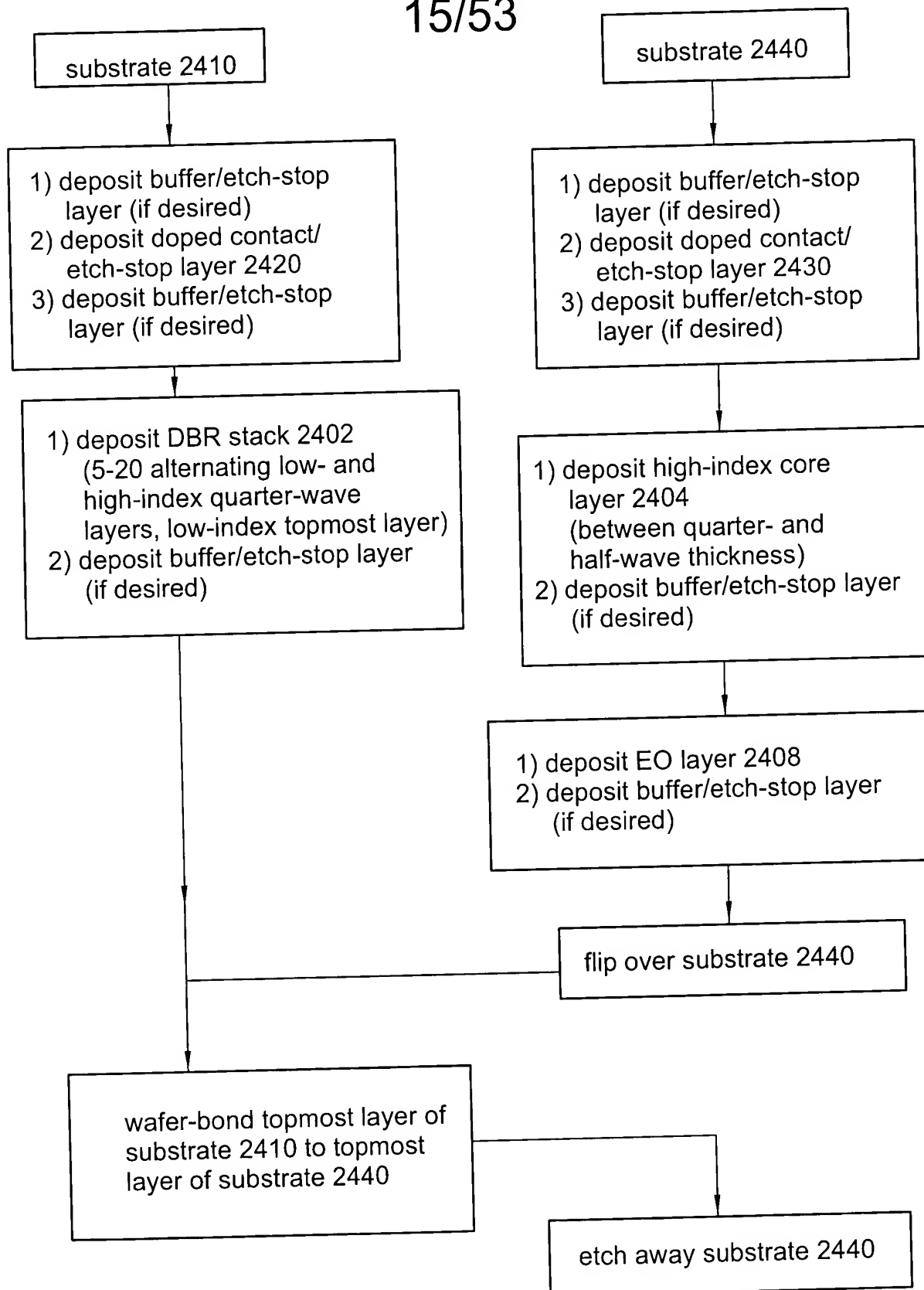


FIG. 23

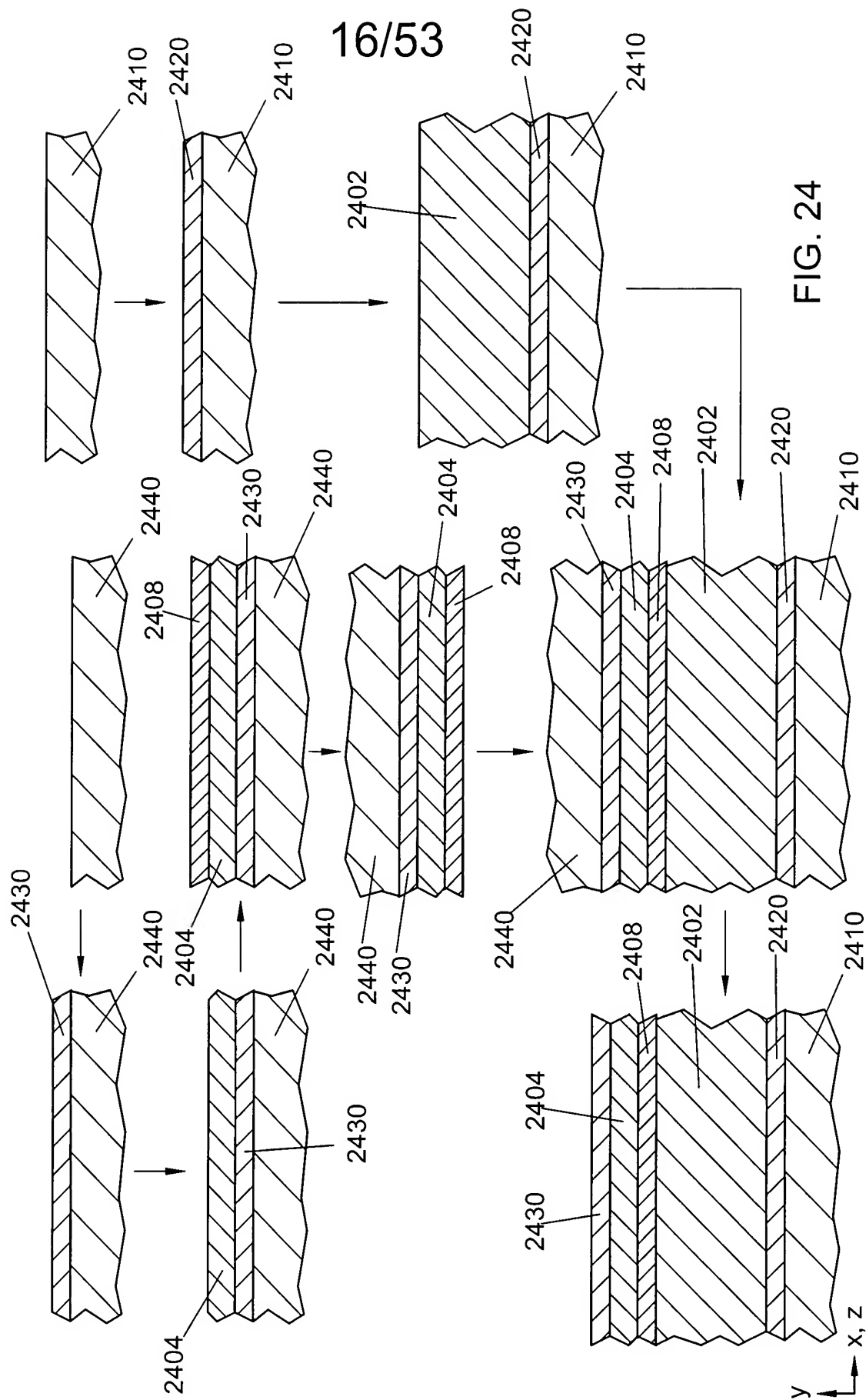


FIG. 24



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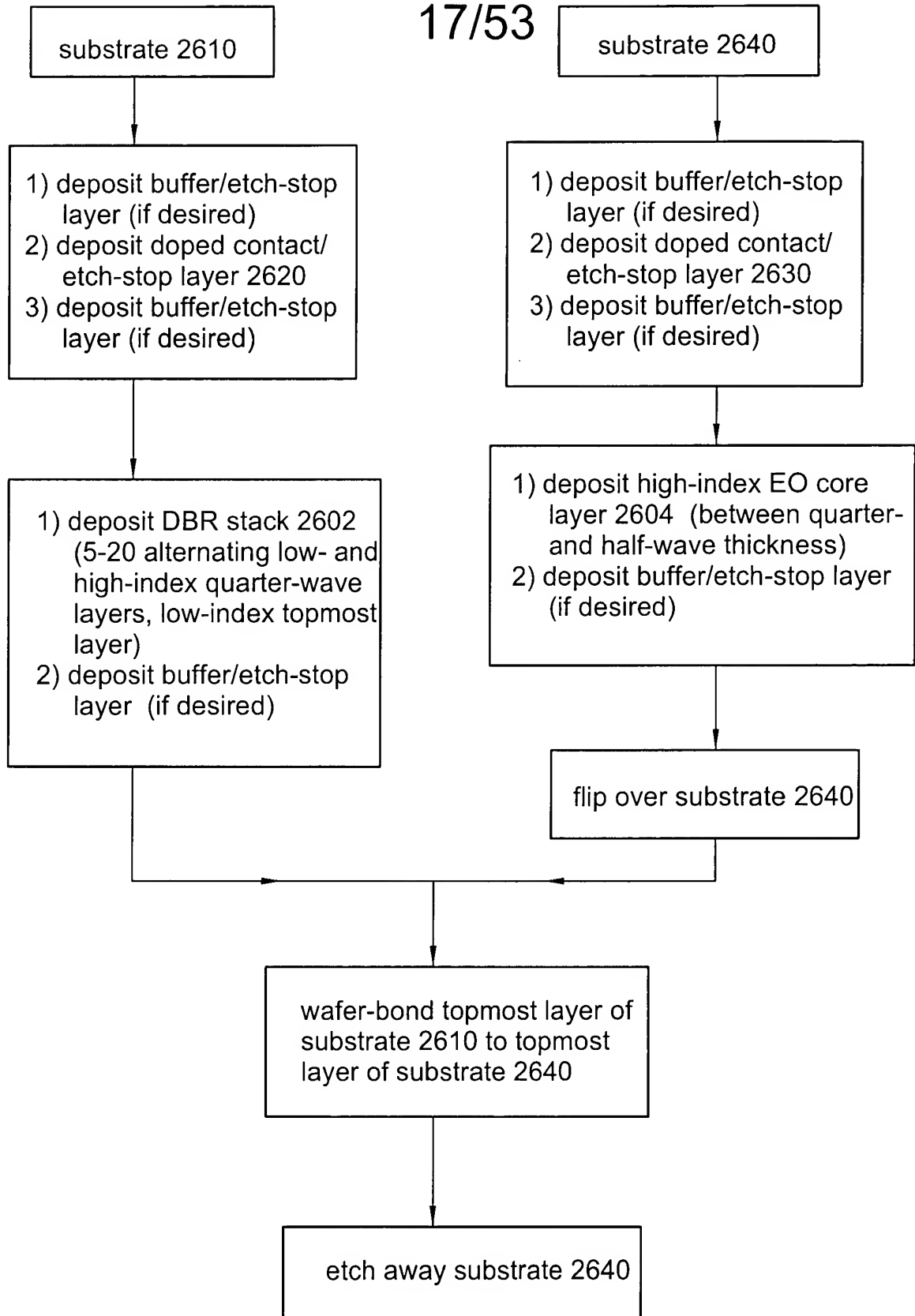
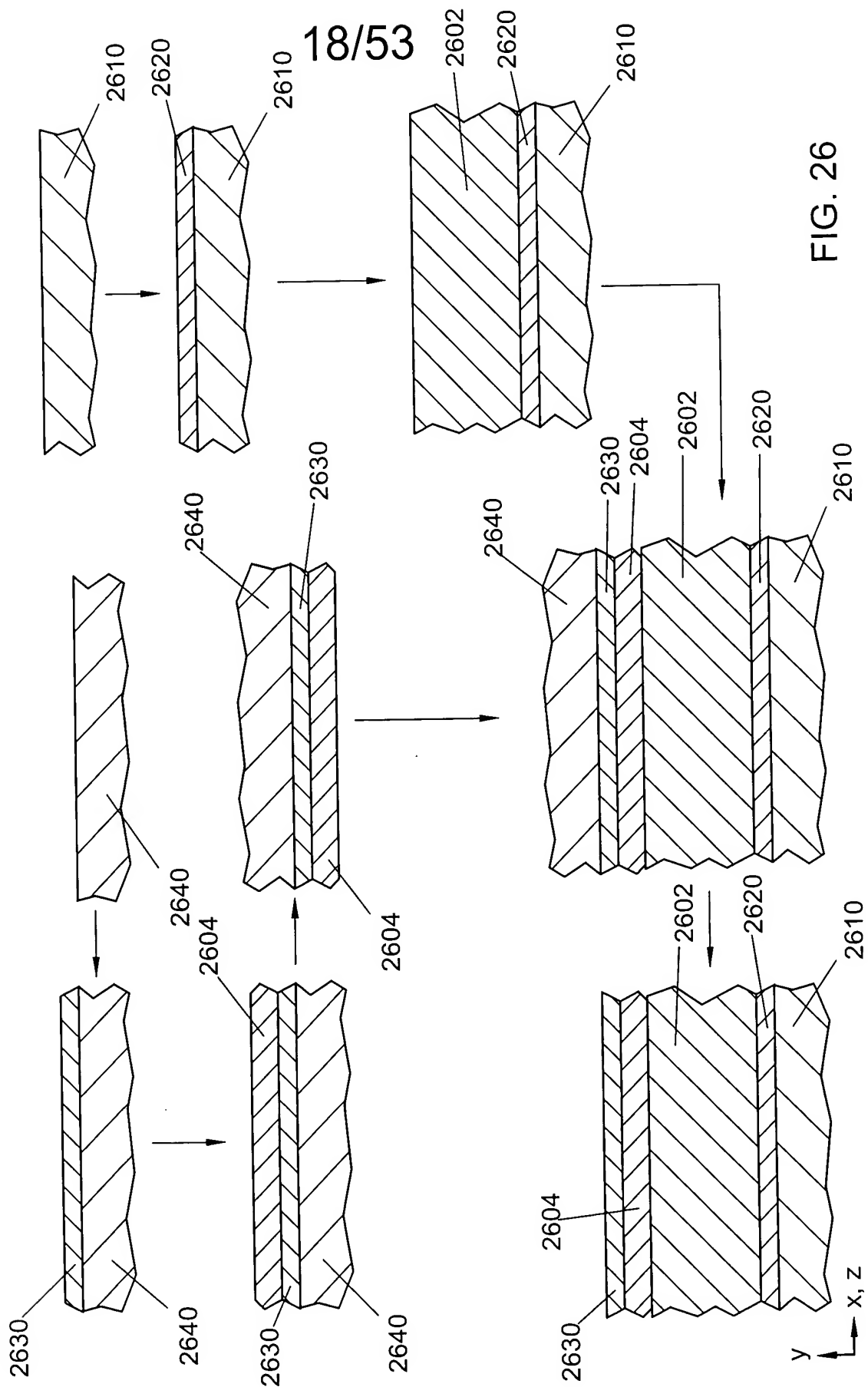


FIG. 25



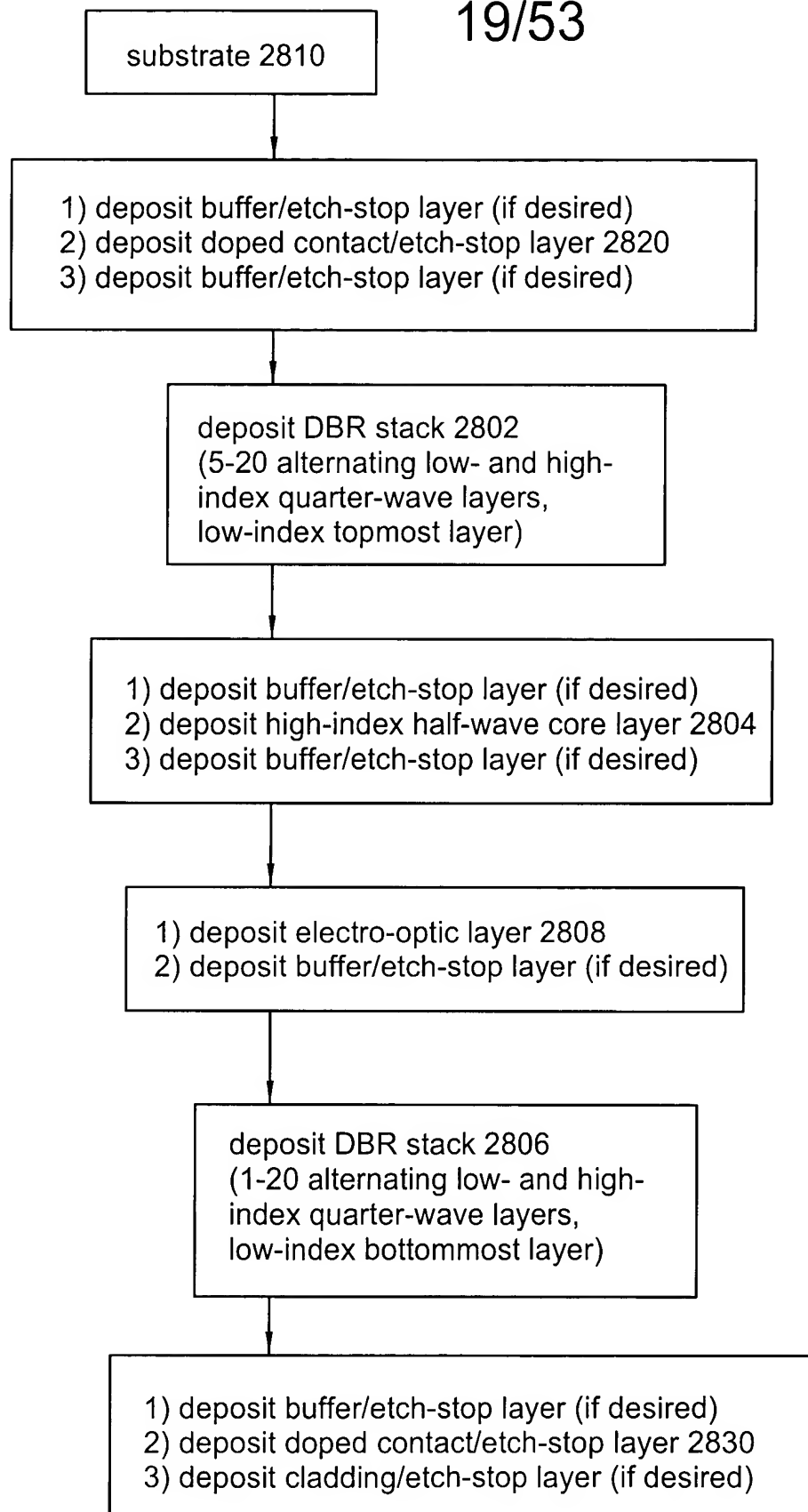
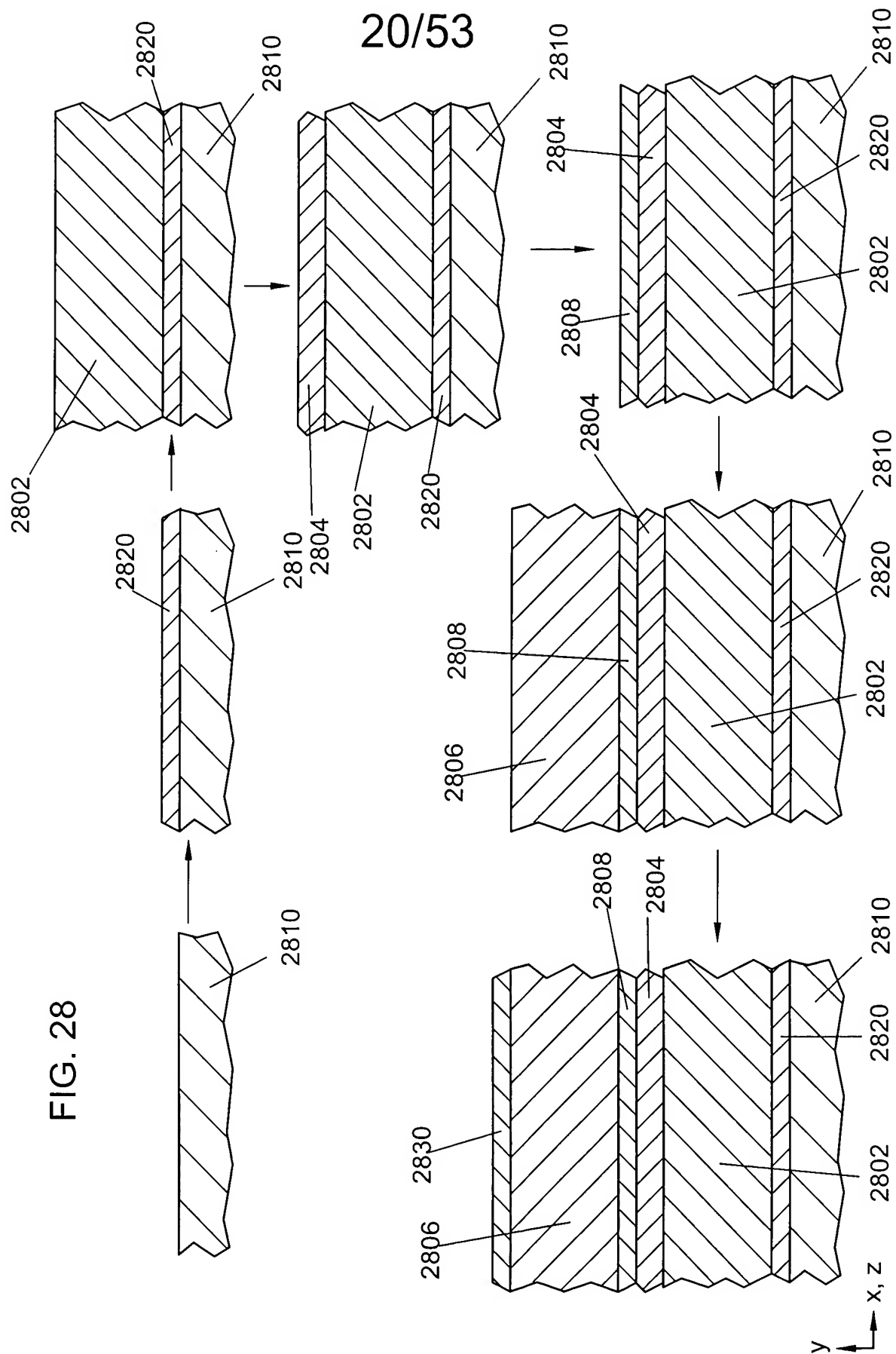


FIG. 27



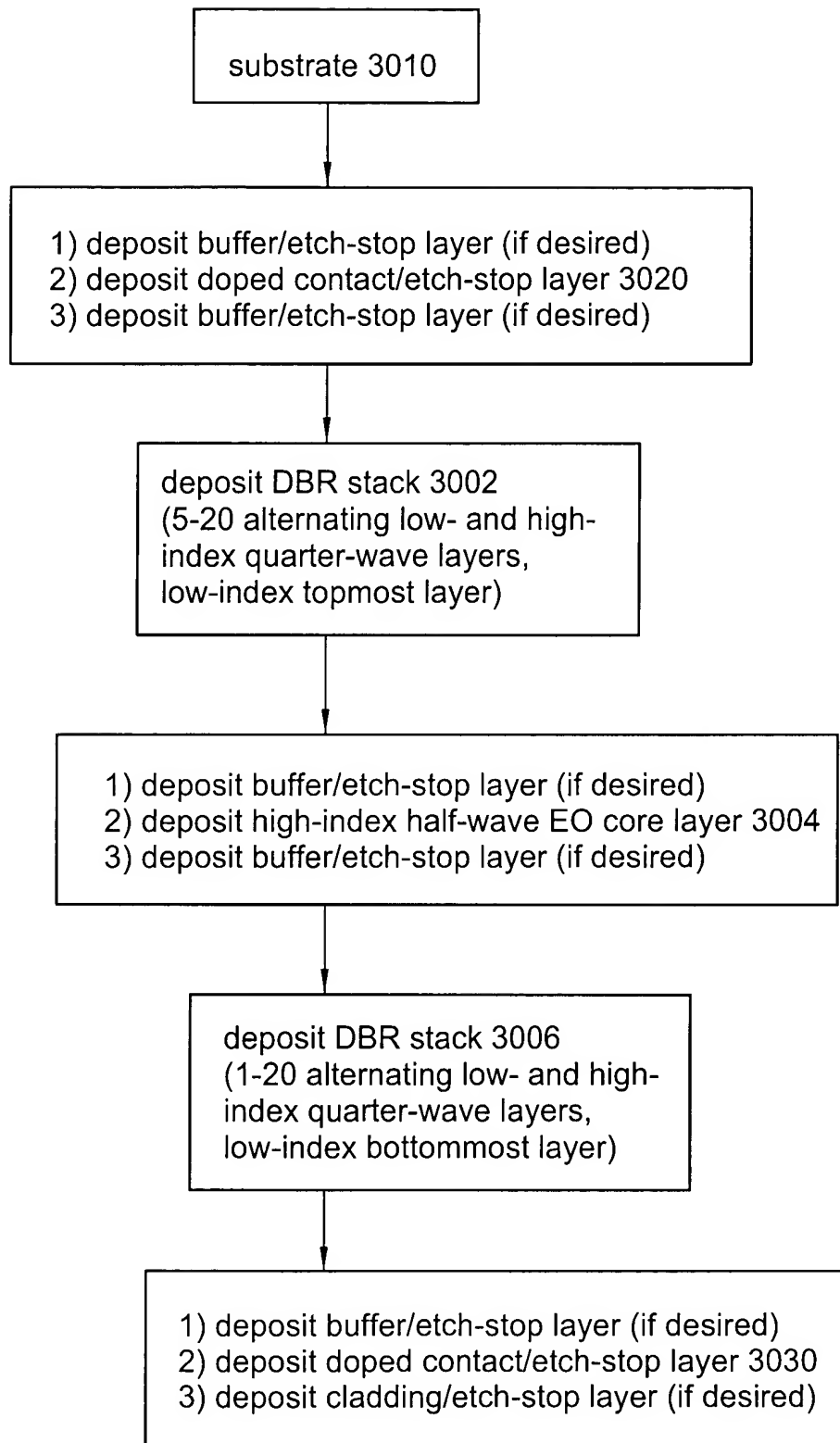
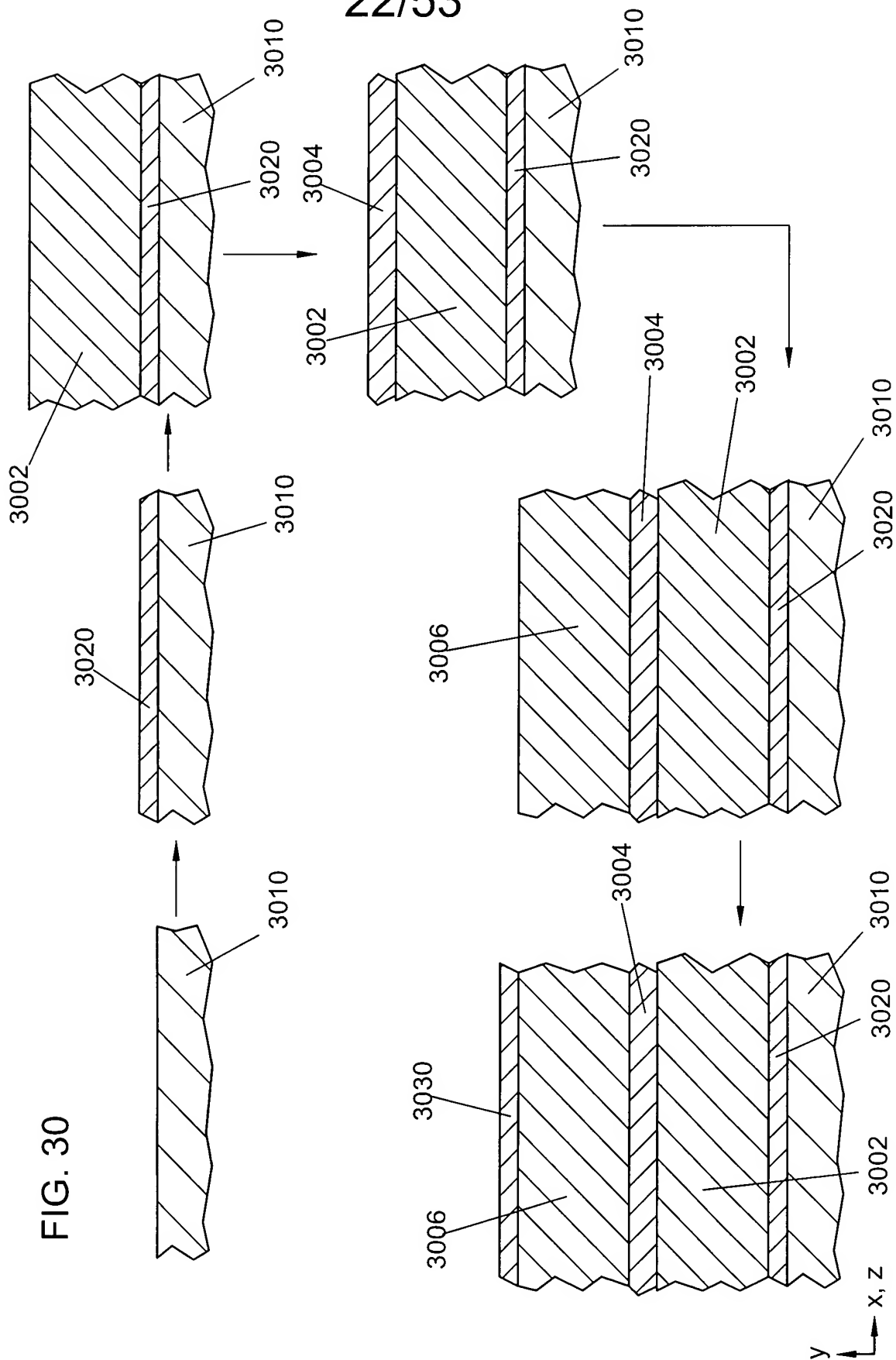


FIG. 29

FIG. 30



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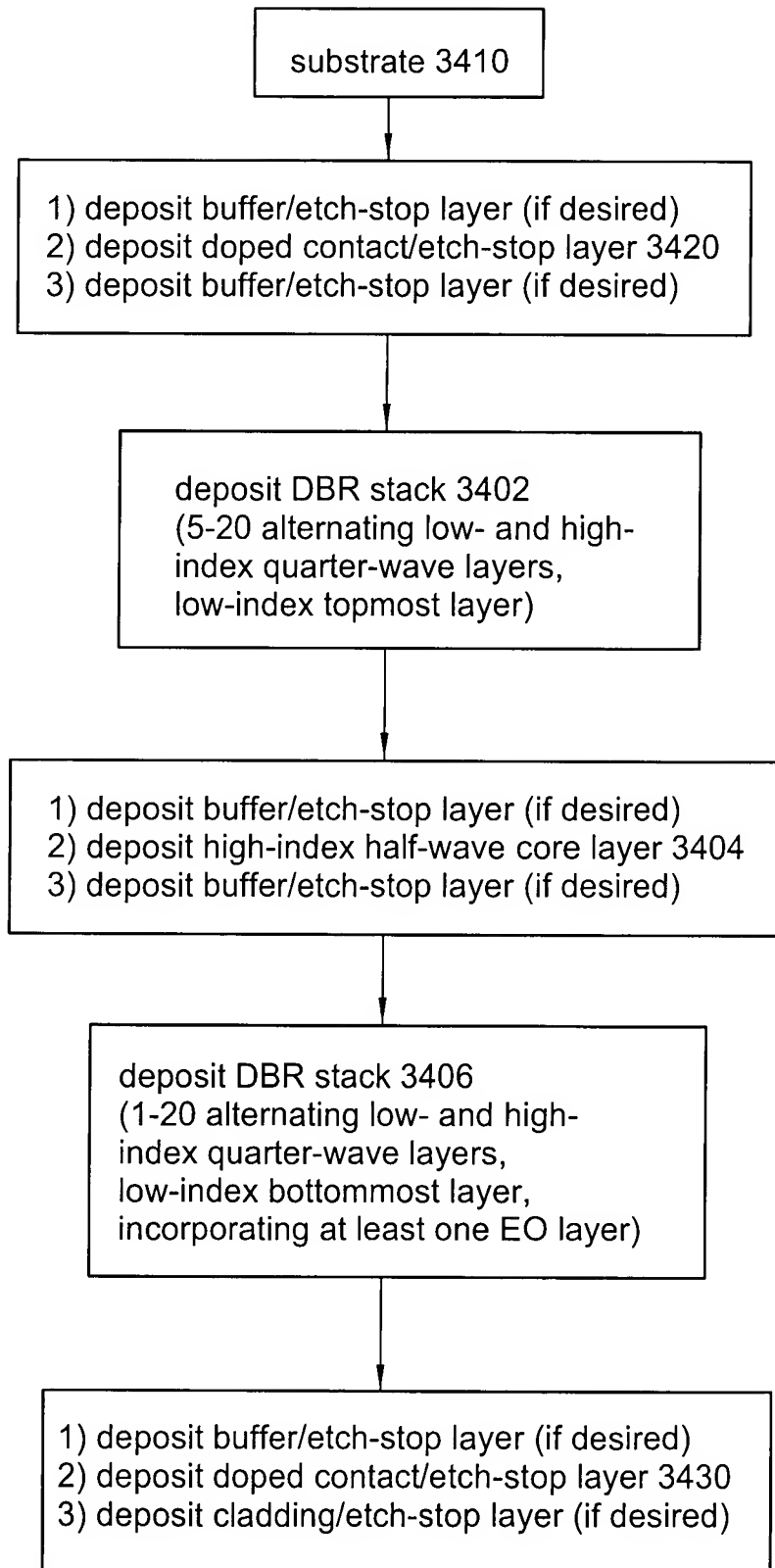


FIG. 31

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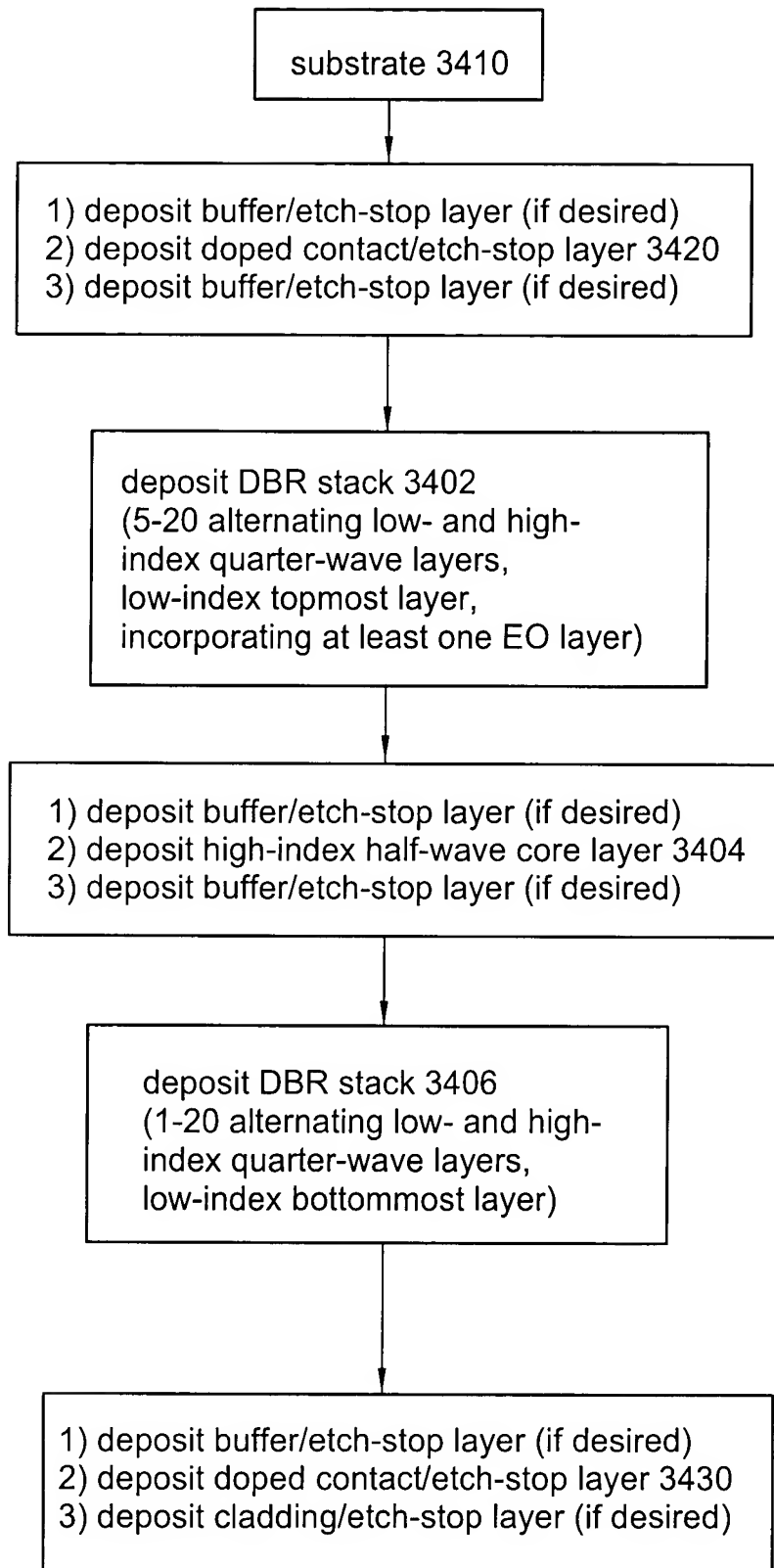


FIG. 32



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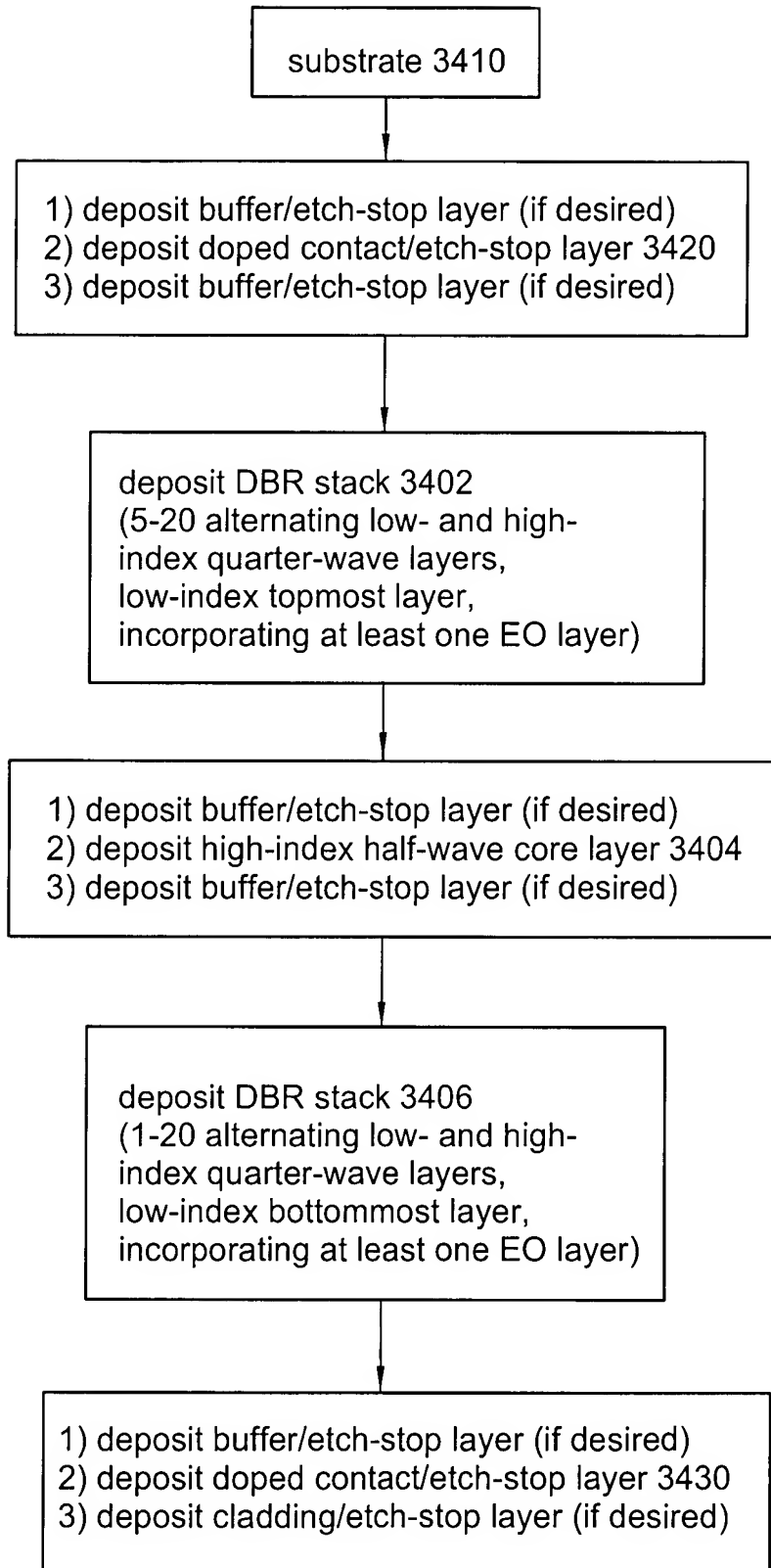
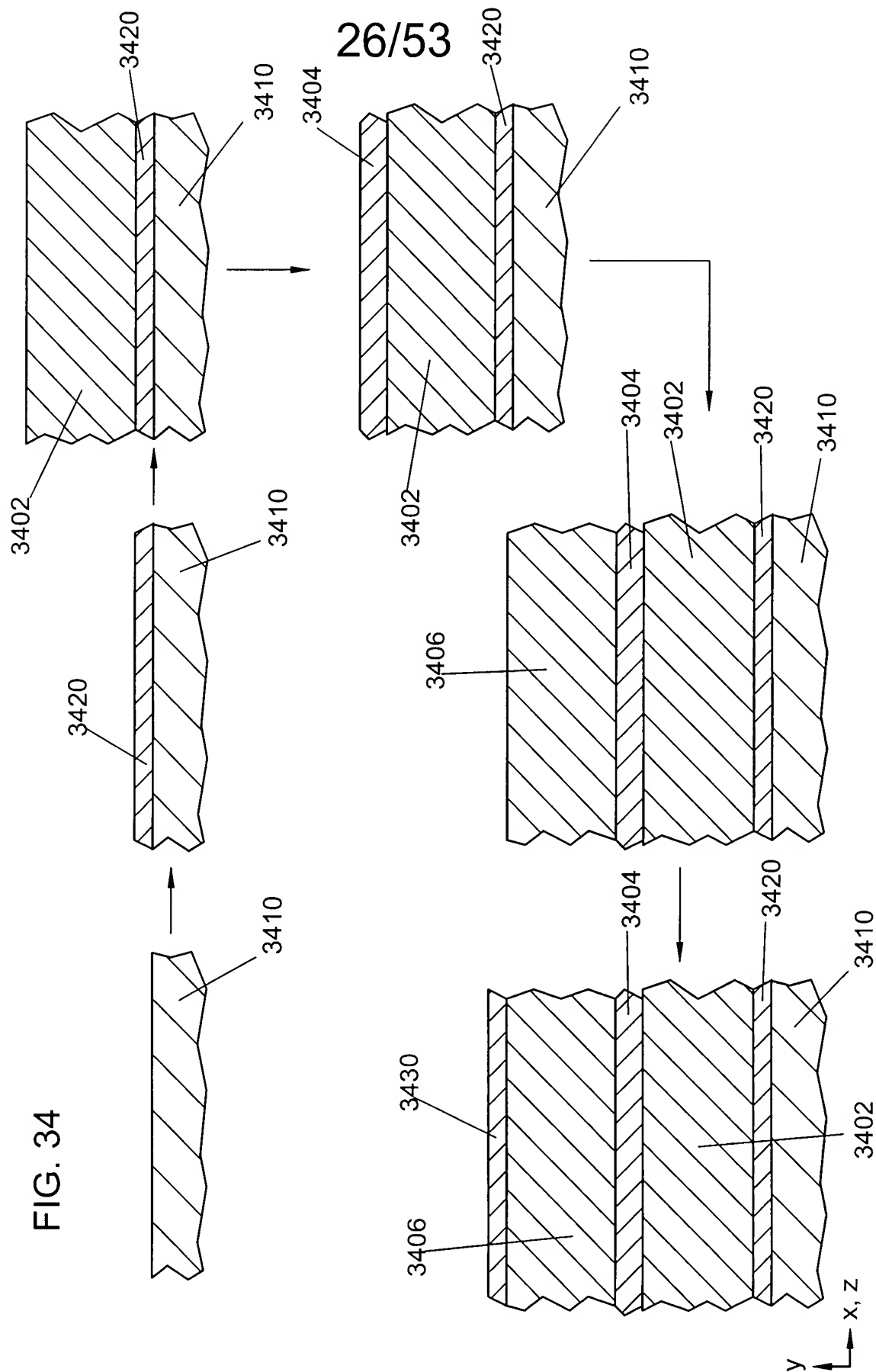


FIG. 33



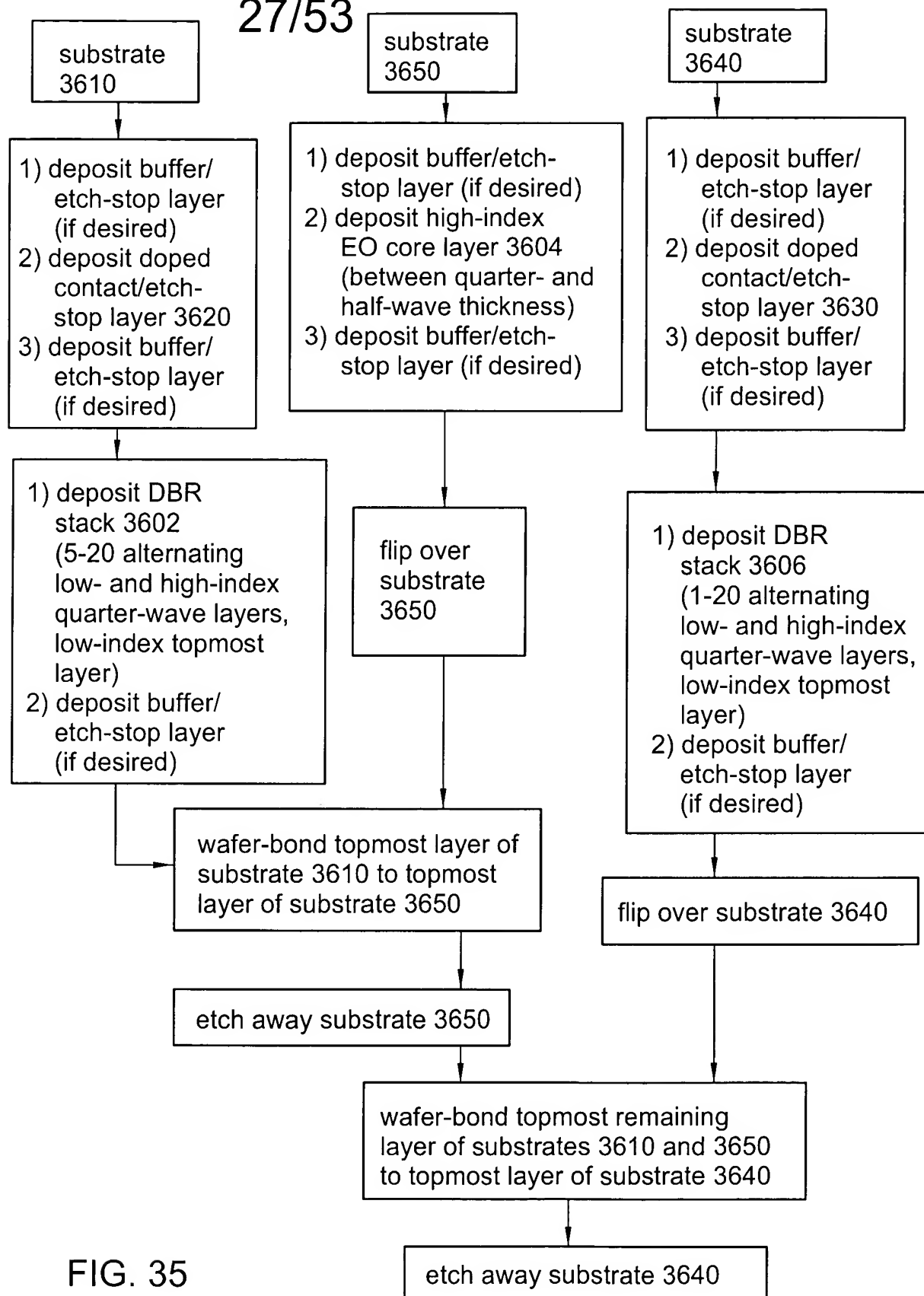


FIG. 35

FIG. 36

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A small diagram showing a coordinate system with x and y axes. A line segment is drawn in the first quadrant, starting from the y-axis and ending at a point labeled 'x'.

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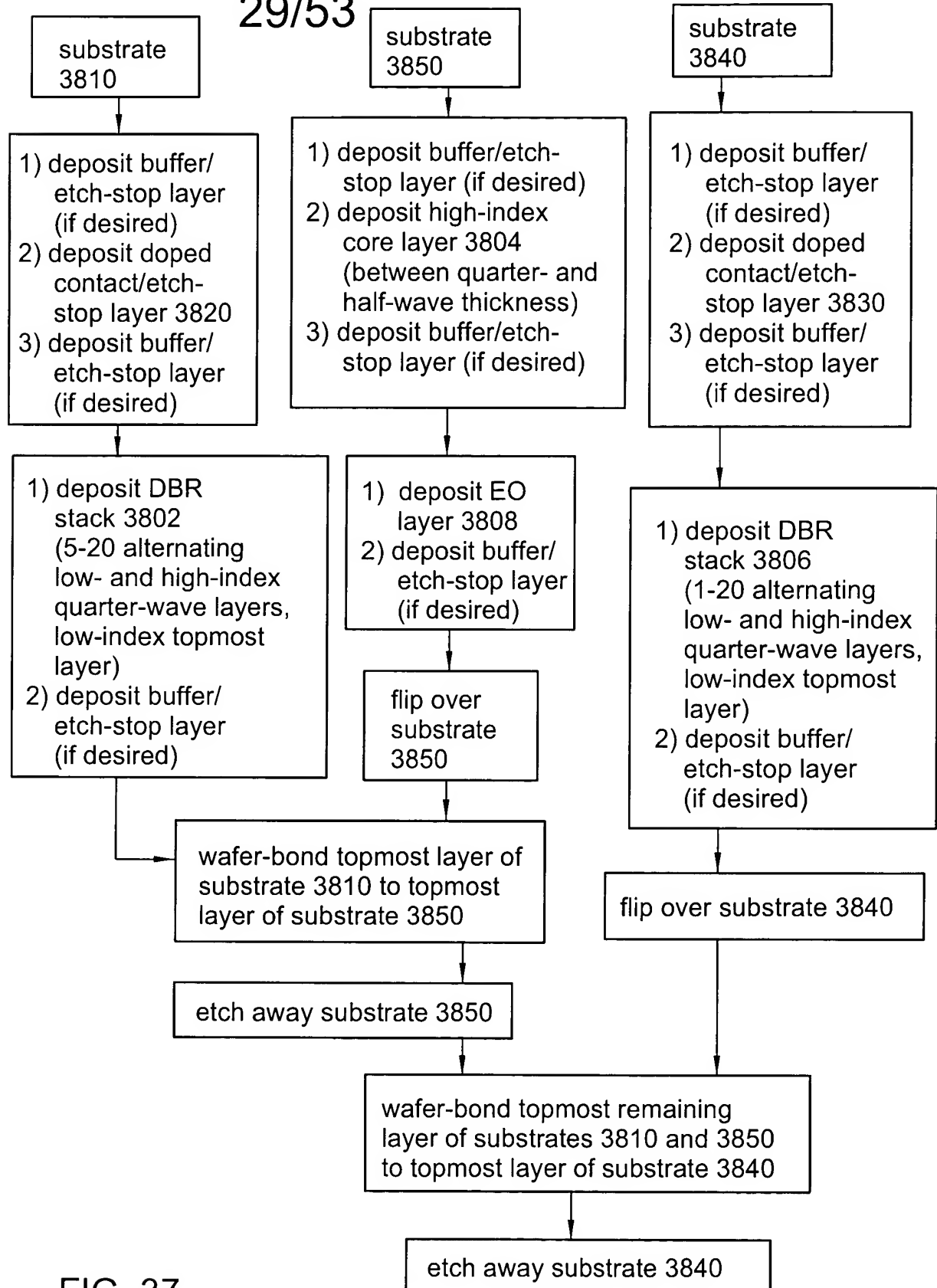
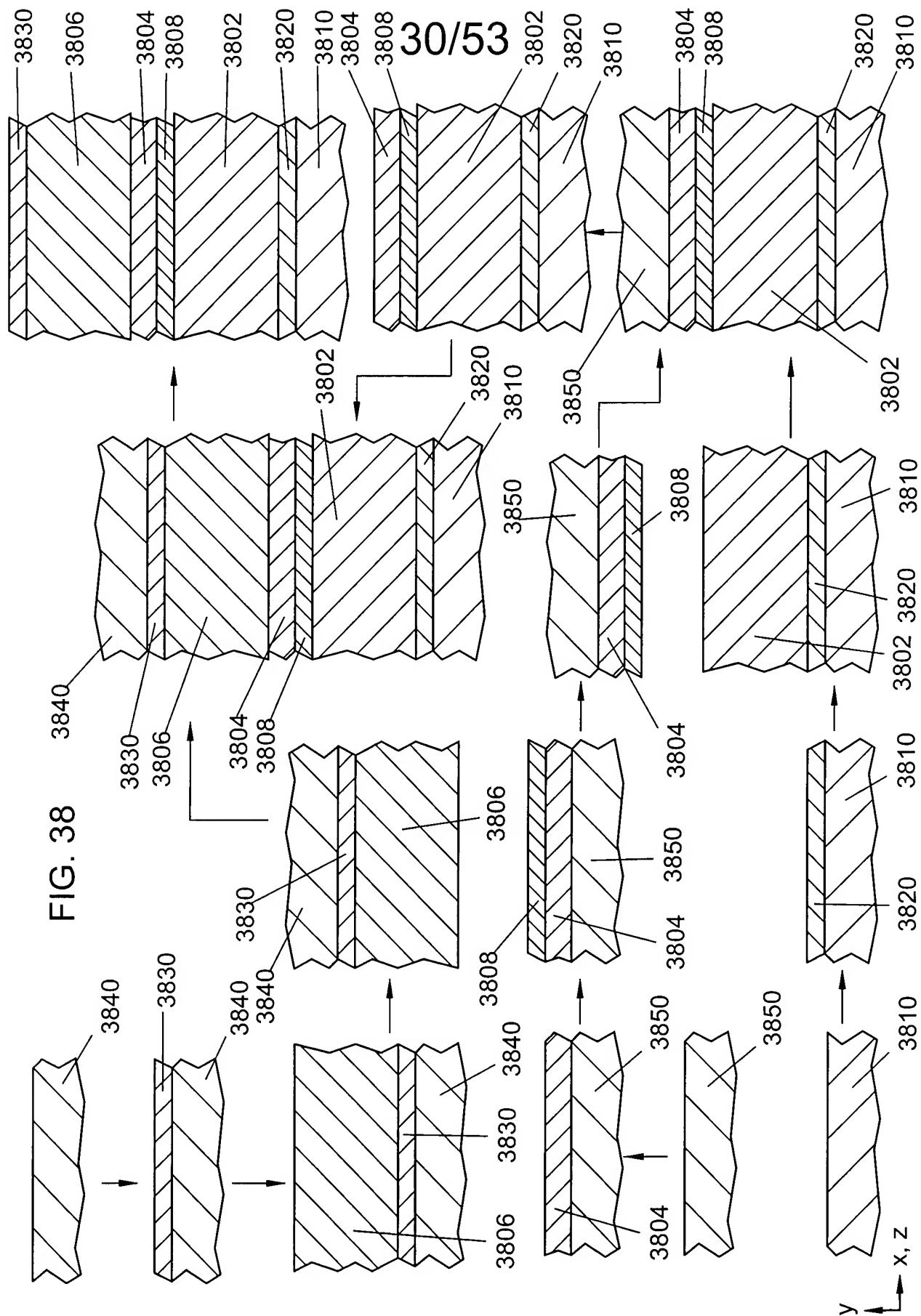


FIG. 37



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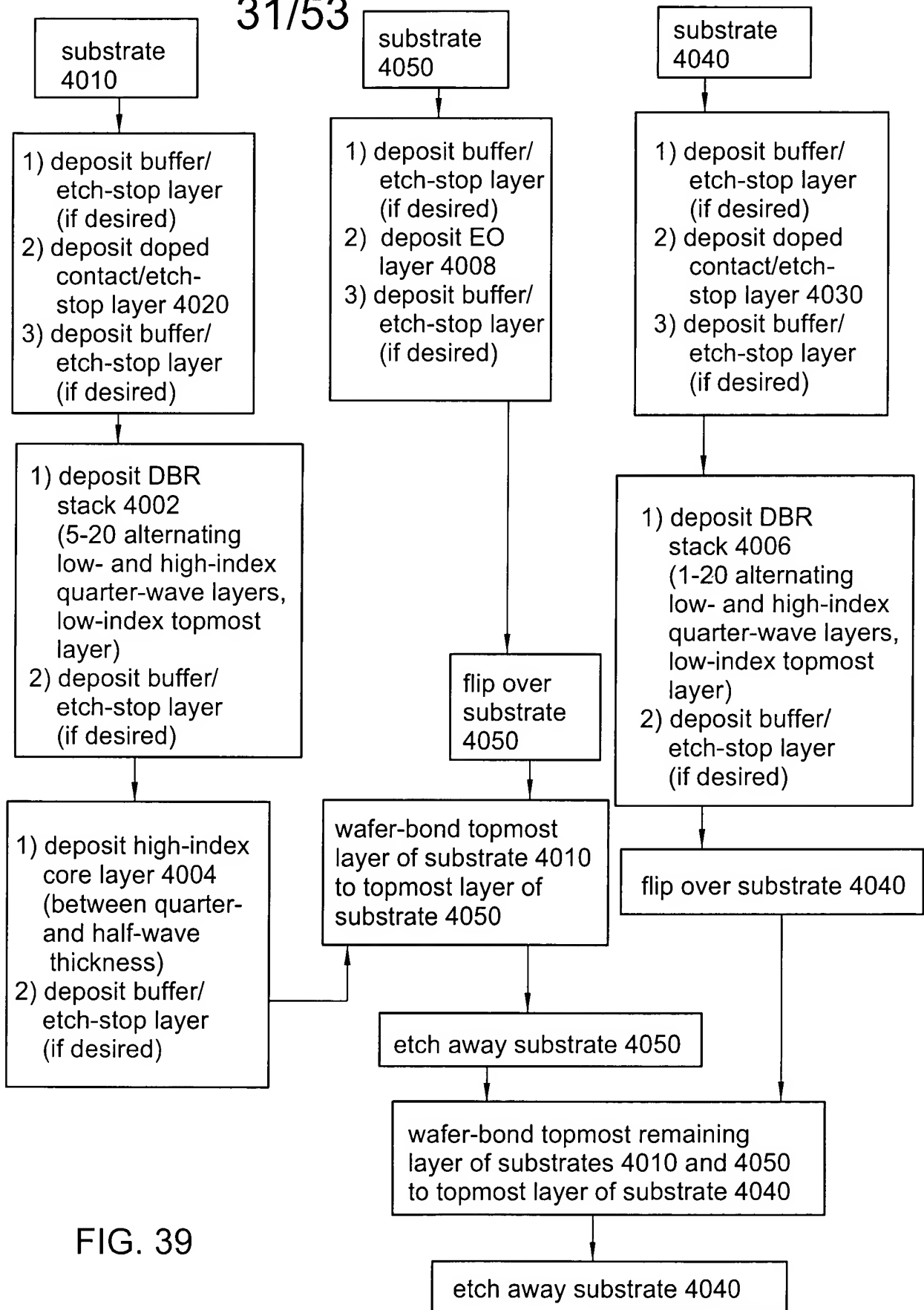


FIG. 39

FIG. 40

FIG. 40 is a series of cross-sectional diagrams illustrating the assembly of a multi-layered structure. The diagrams show the sequential addition of layers 4030, 4006, 4040, 4008, 4004, 4002, 4020, 4010, and 4008. The final structure is a complex, multi-layered assembly with a wavy top surface. A coordinate system (x, y, z) is shown at the bottom right.



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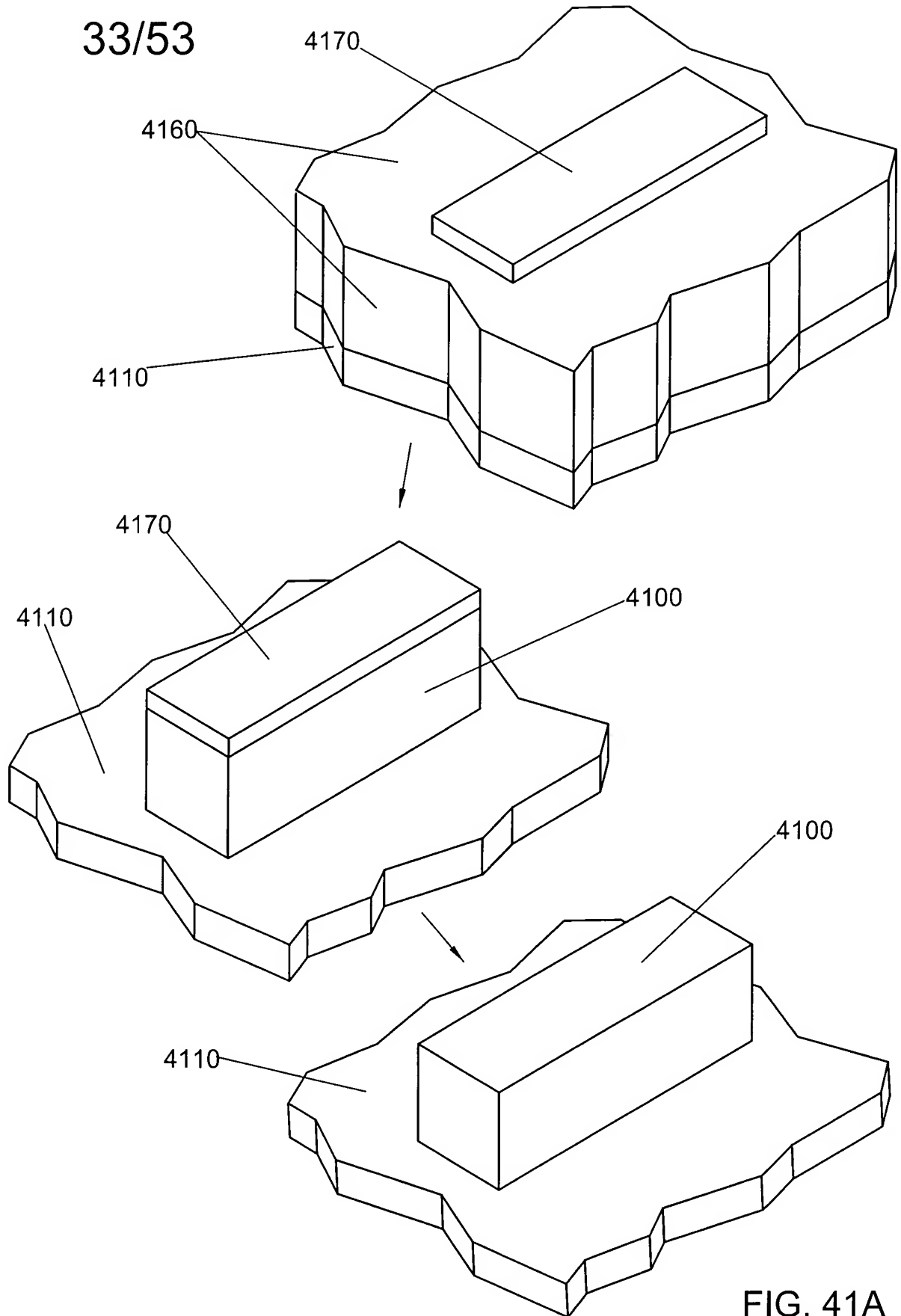


FIG. 41A

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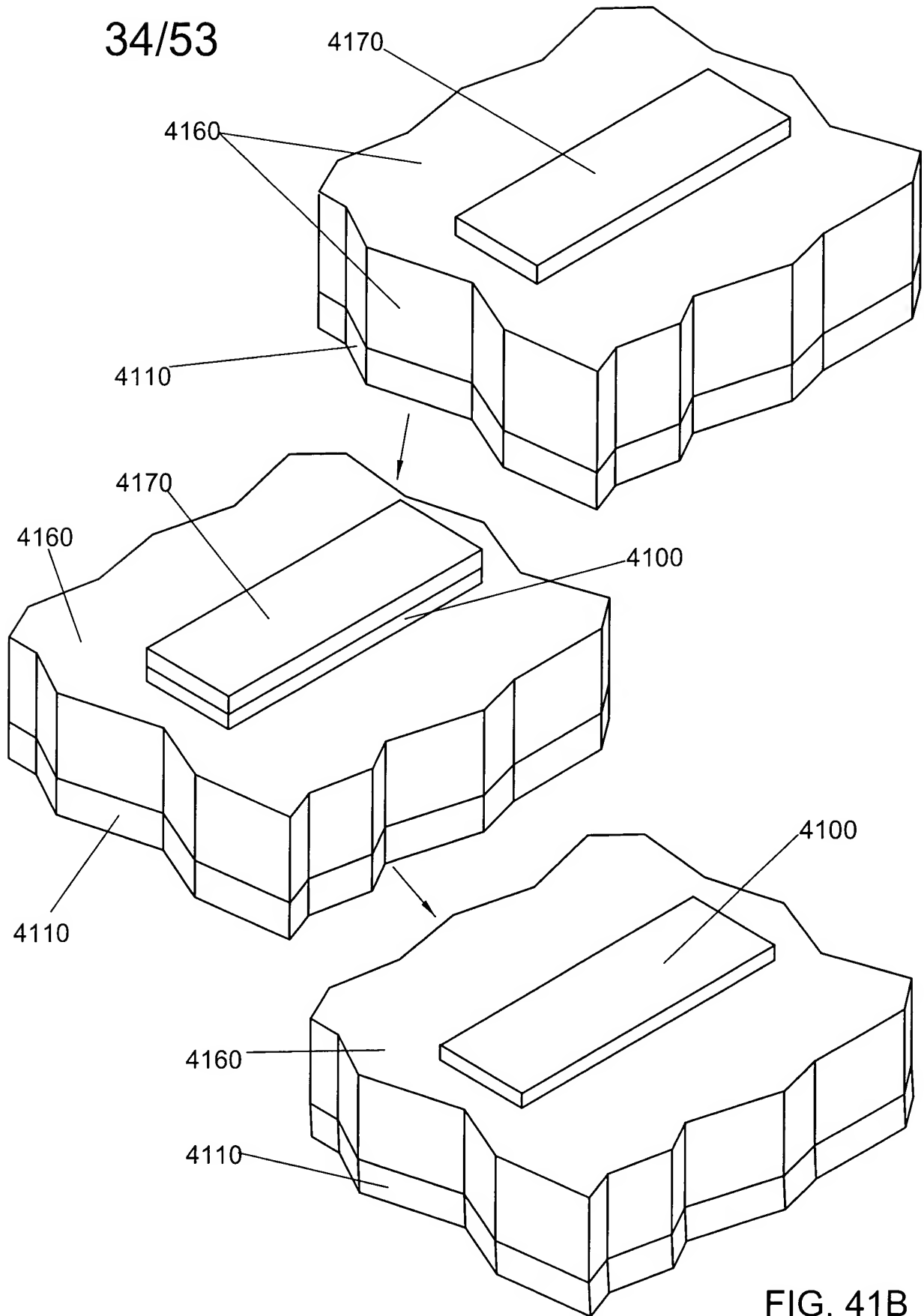


FIG. 41B

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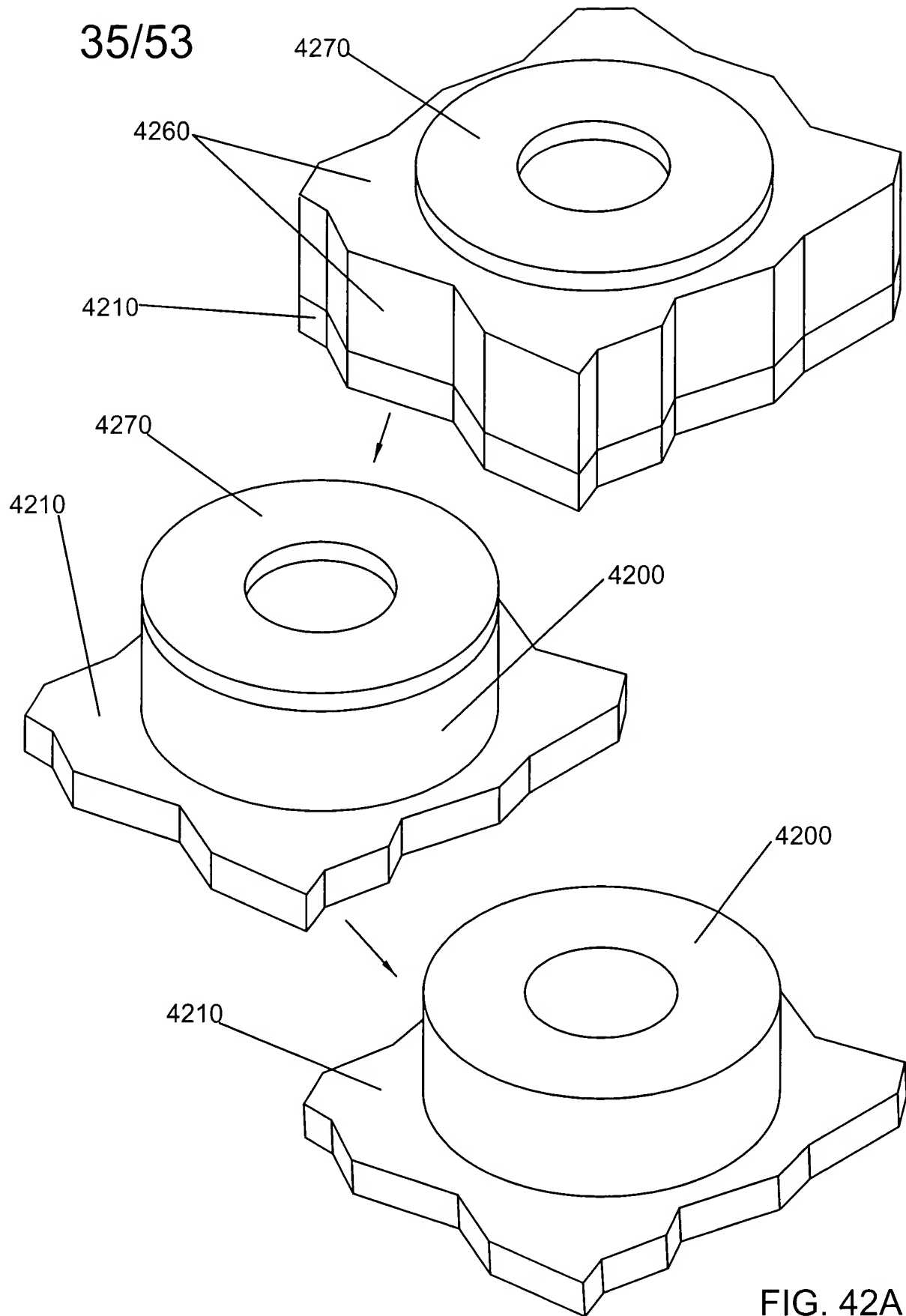


FIG. 42A

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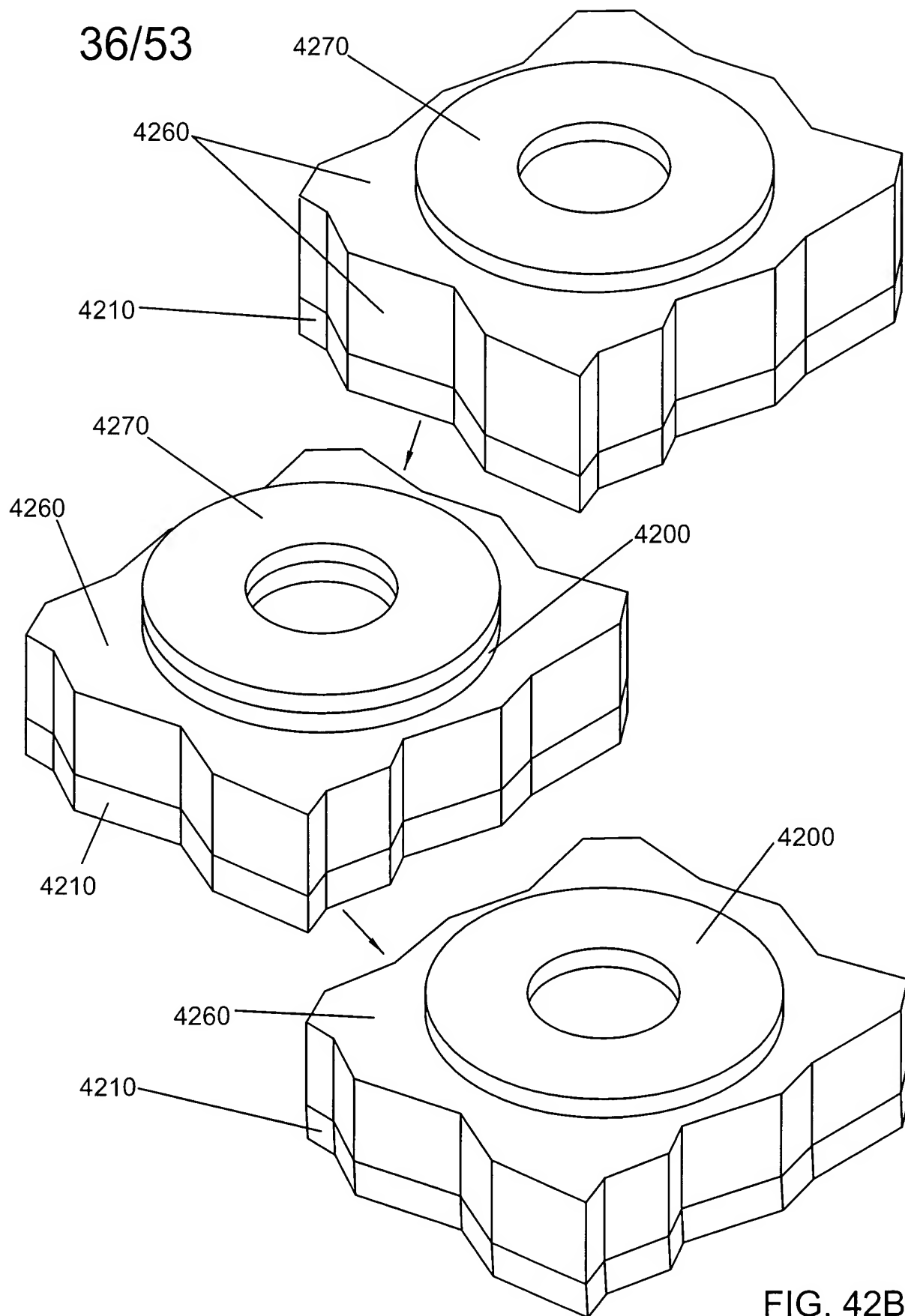


FIG. 42B

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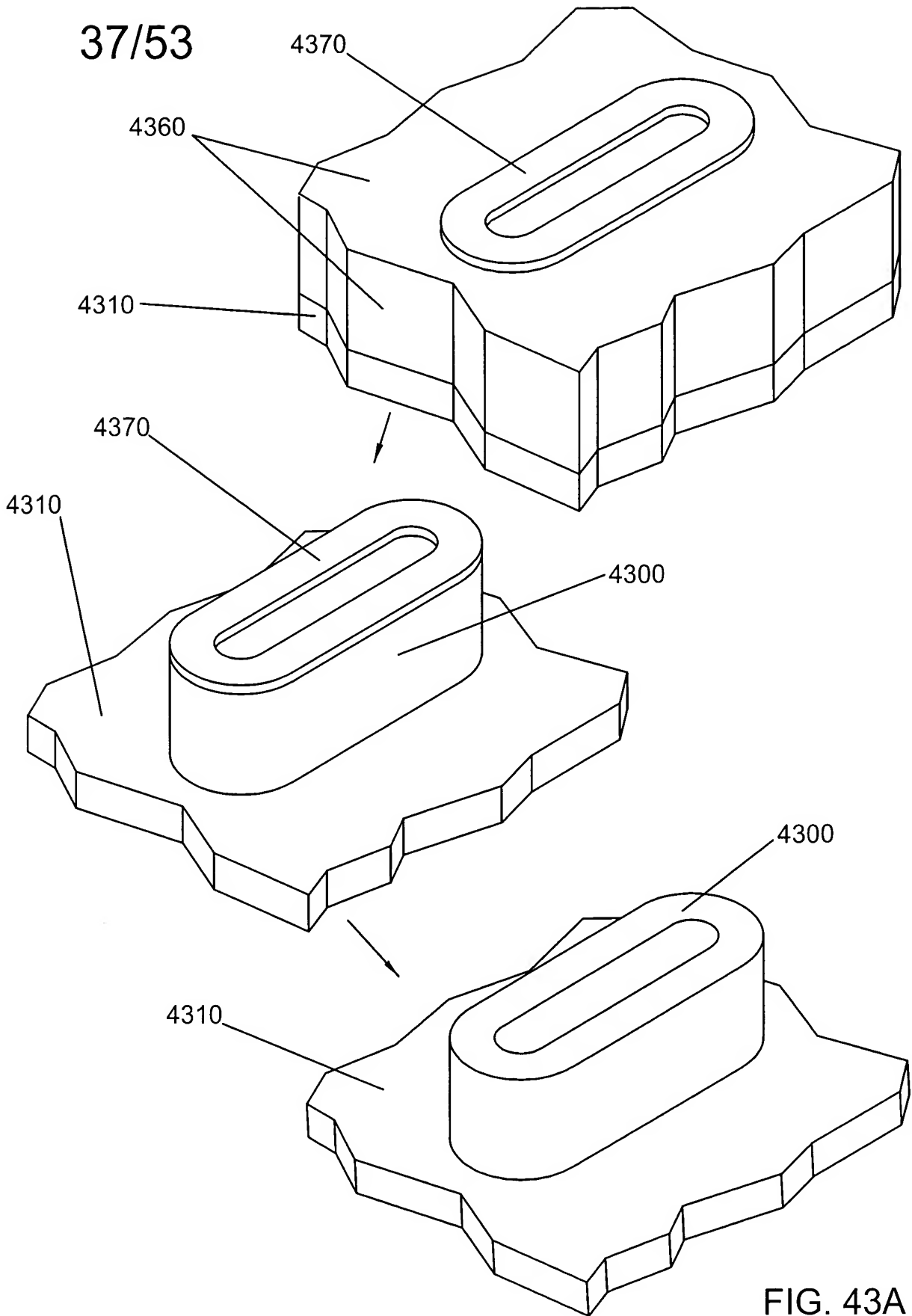


FIG. 43A

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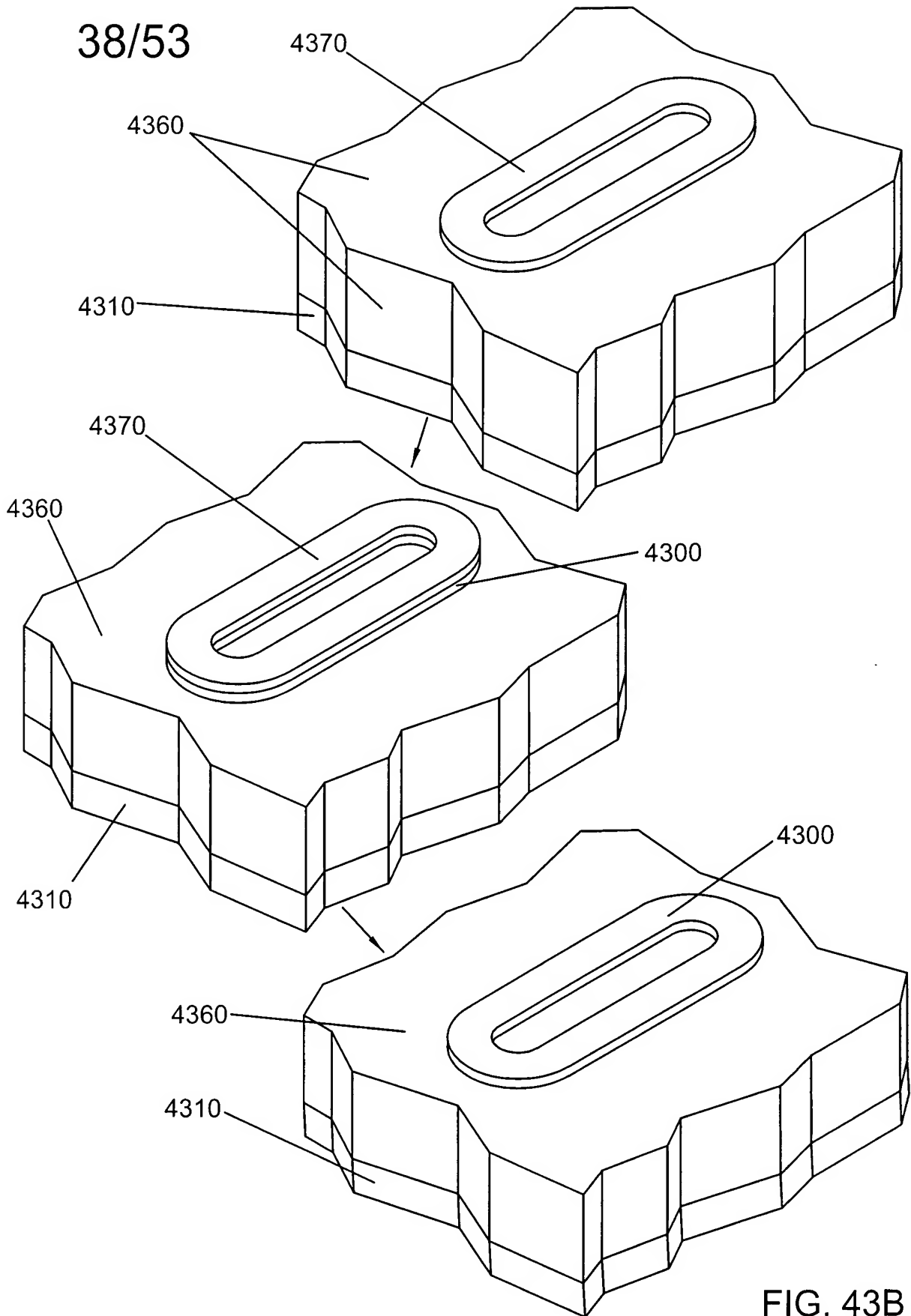


FIG. 43B

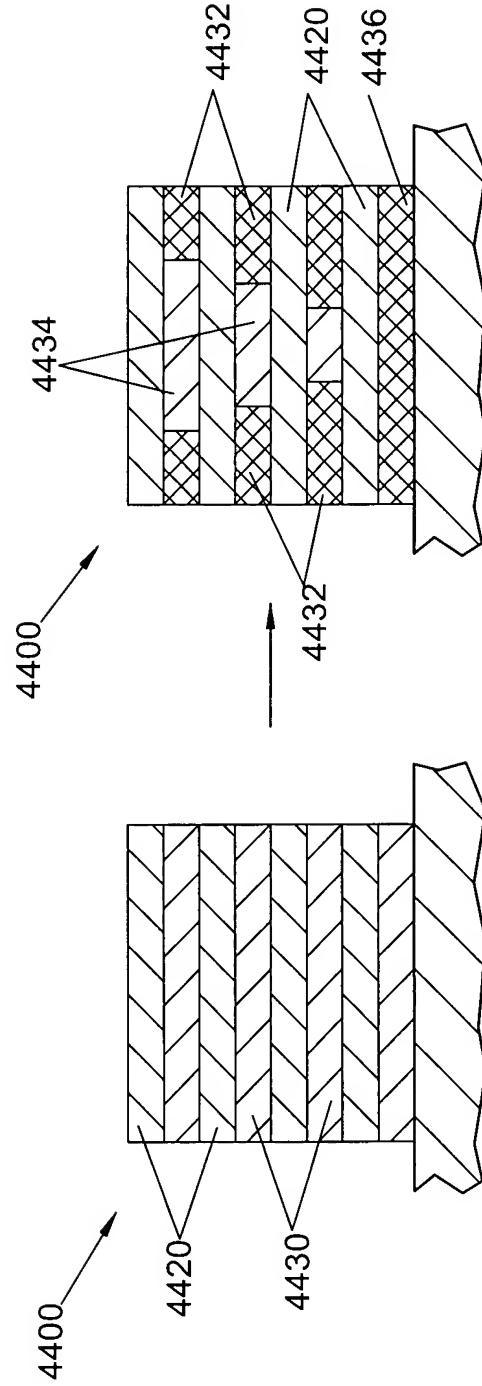
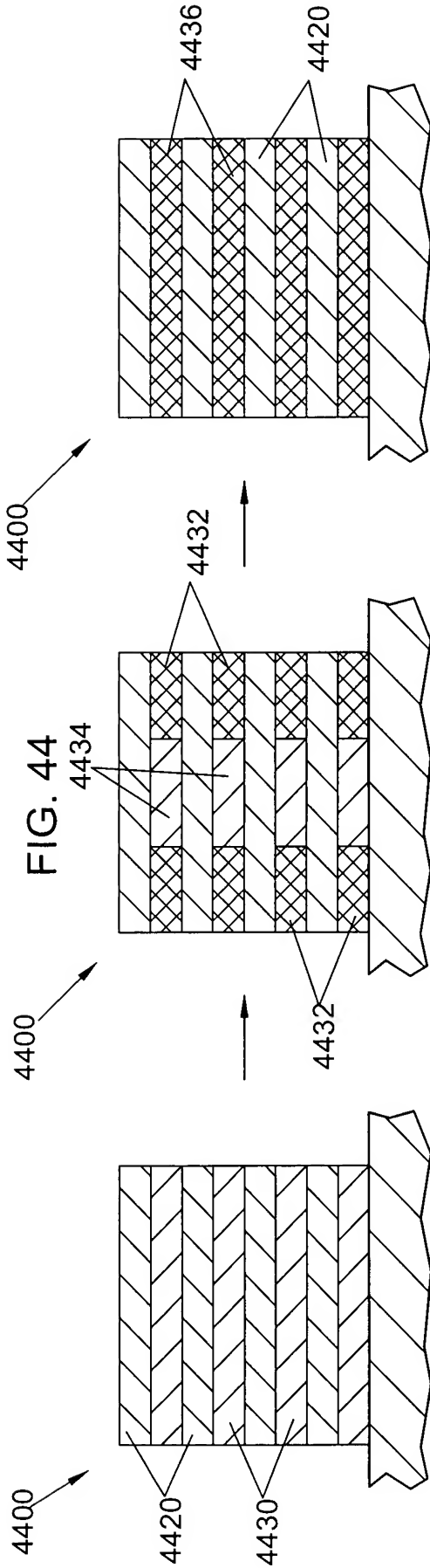


FIG. 46

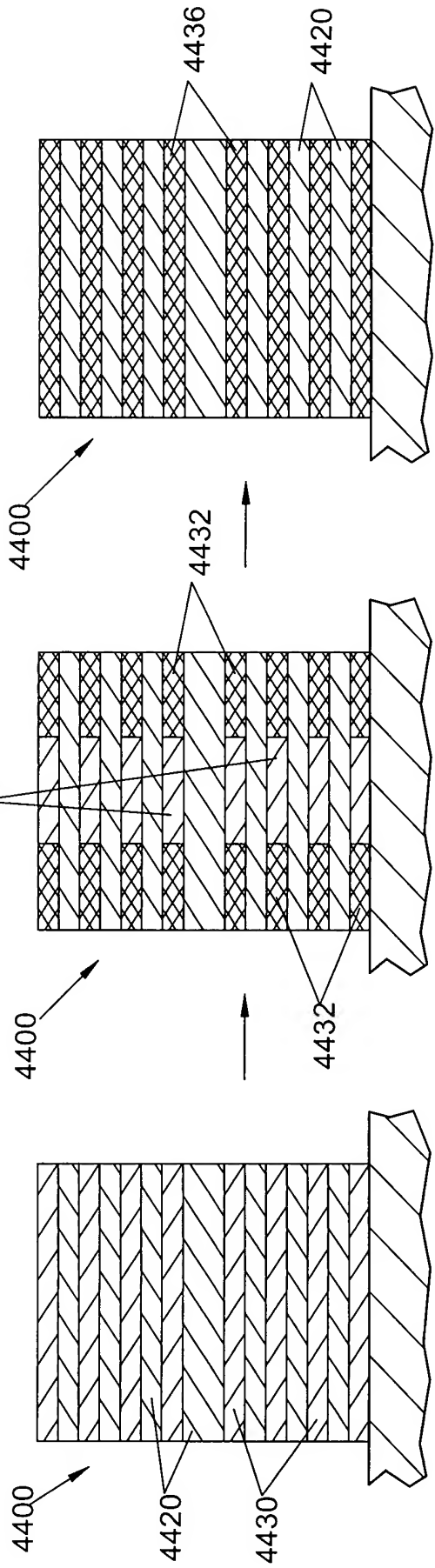


FIG. 47

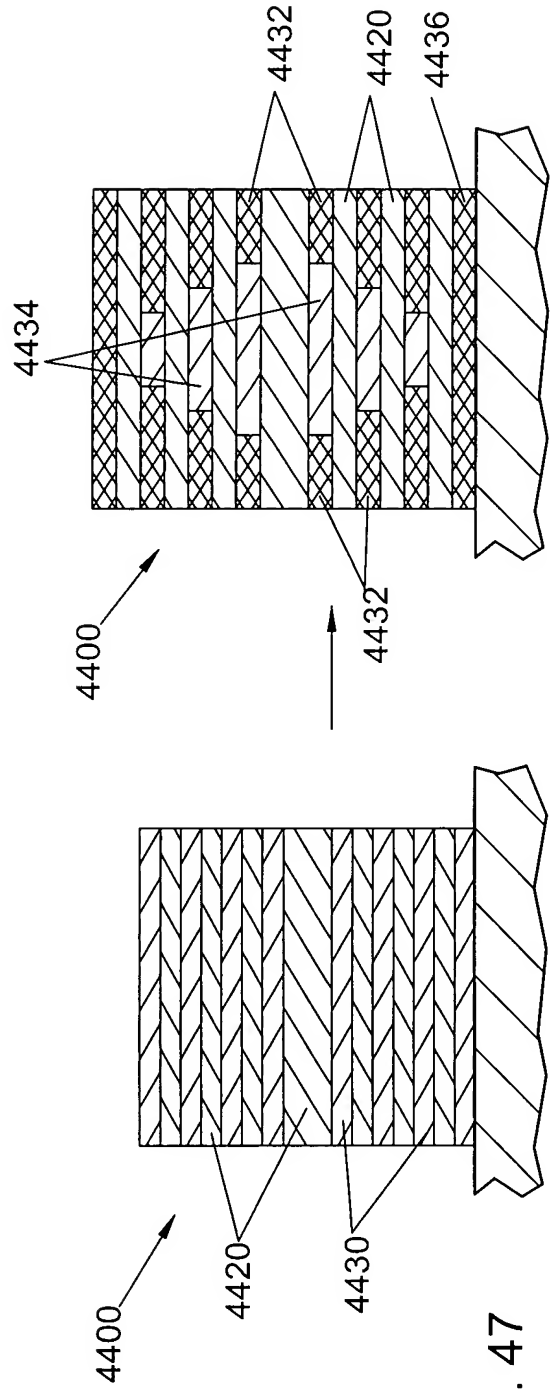




FIG. 48

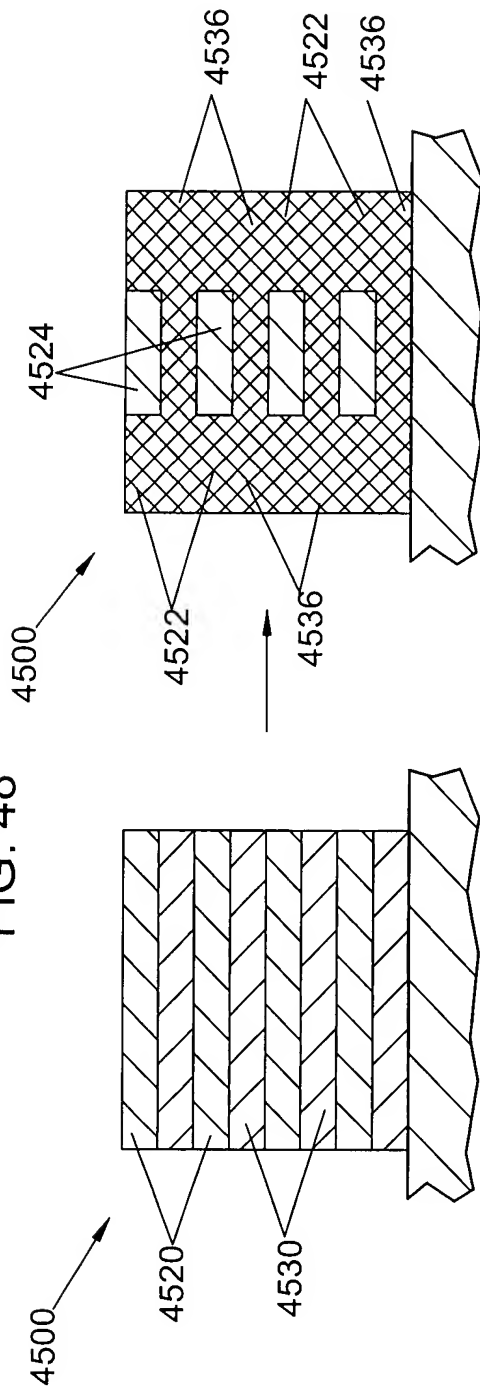


FIG. 49

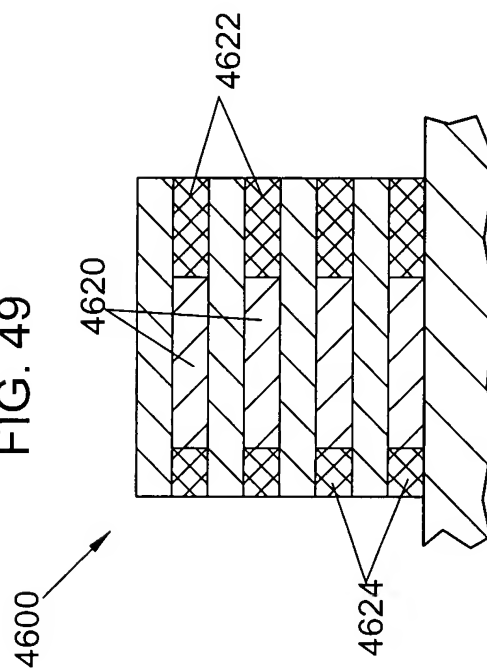


FIG. 50

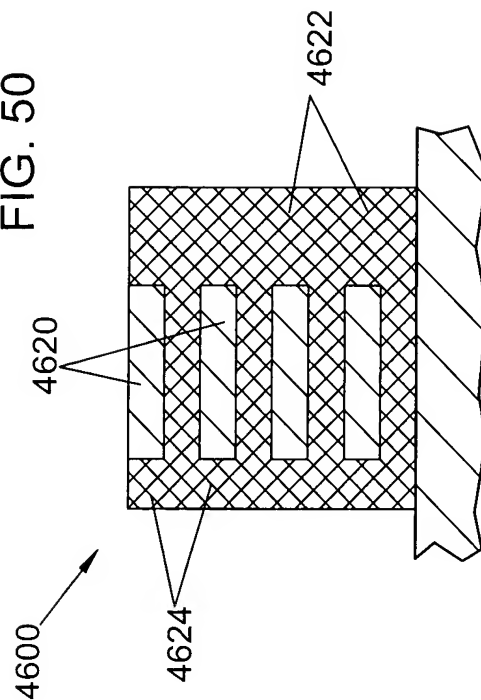
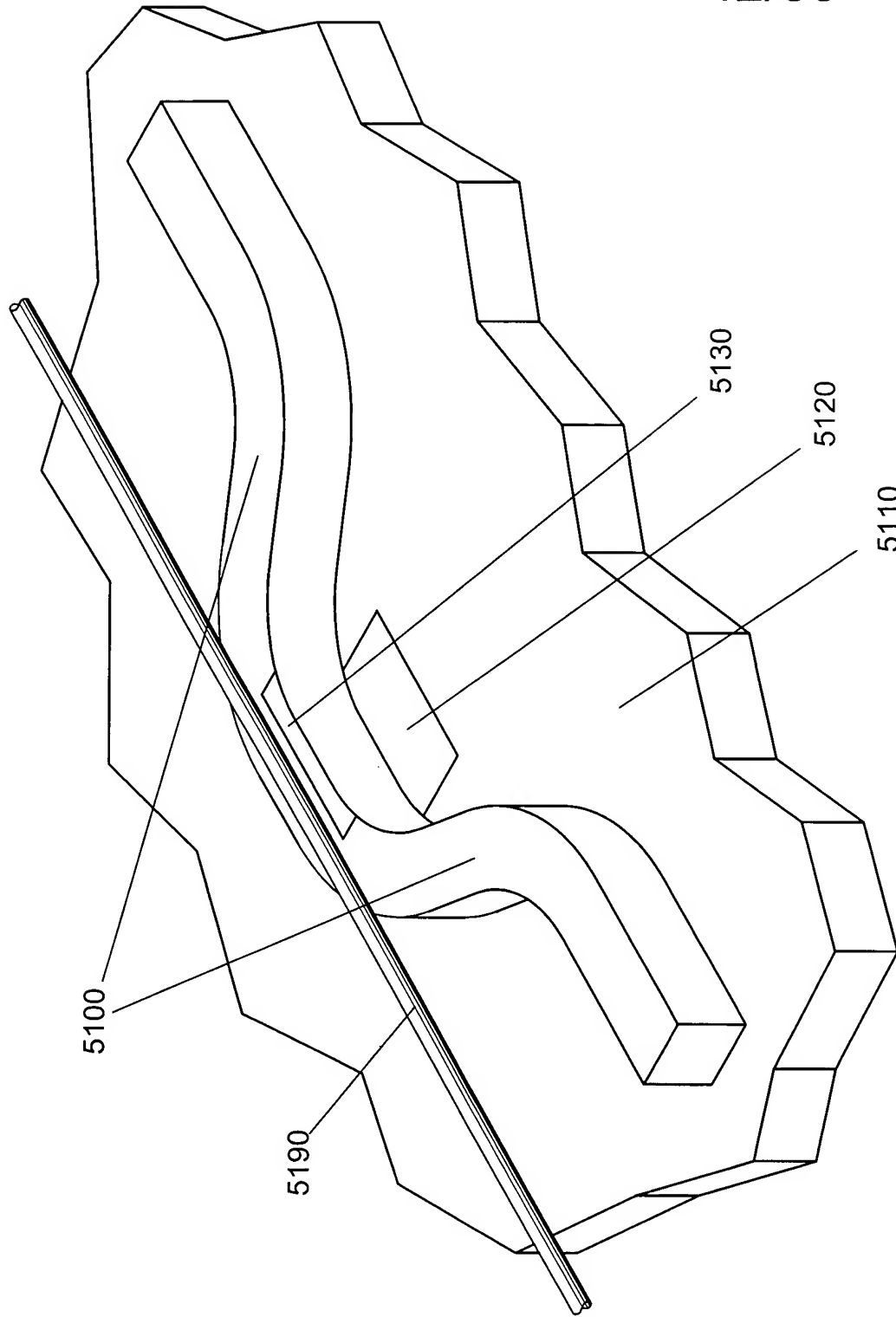


FIG. 51



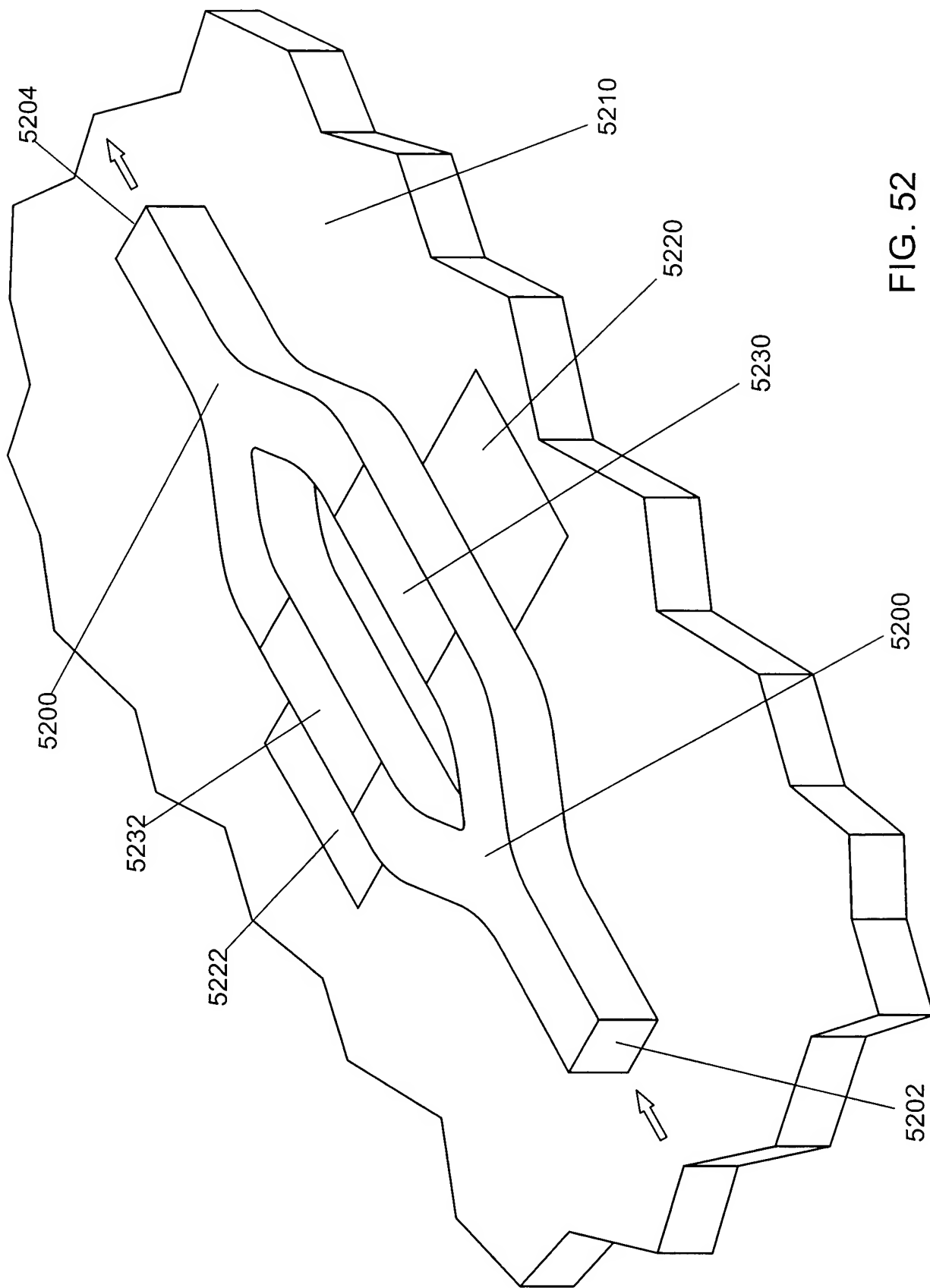
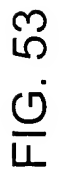


FIG. 52



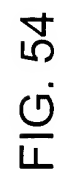


FIG. 55 is a perspective view of the device 46/53 in a folded state, showing the device 46/53 in a folded state, with the device 46/53 in a folded state, and the device 46/53 in a folded state.

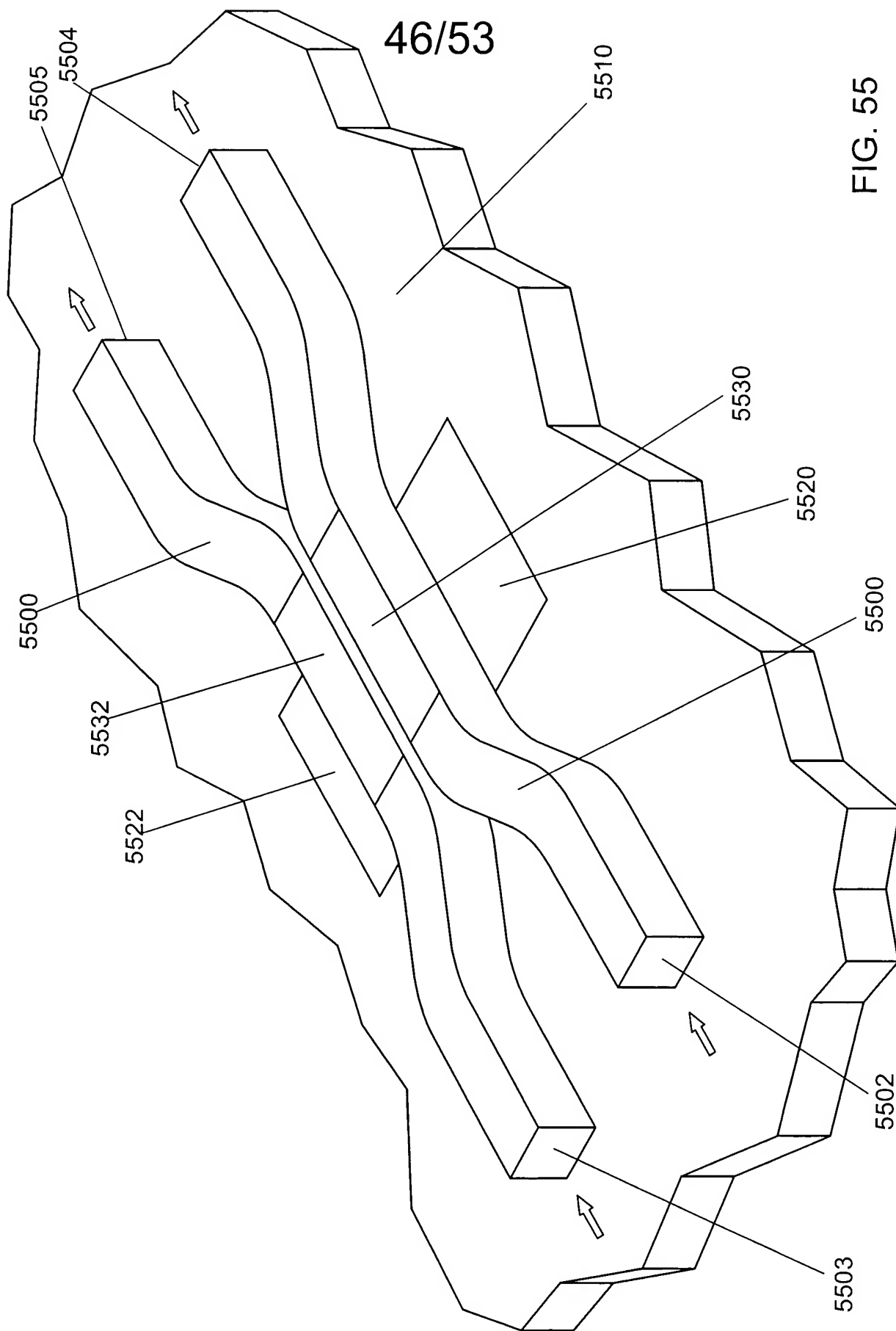
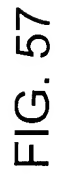


FIG. 55



FIG. 56





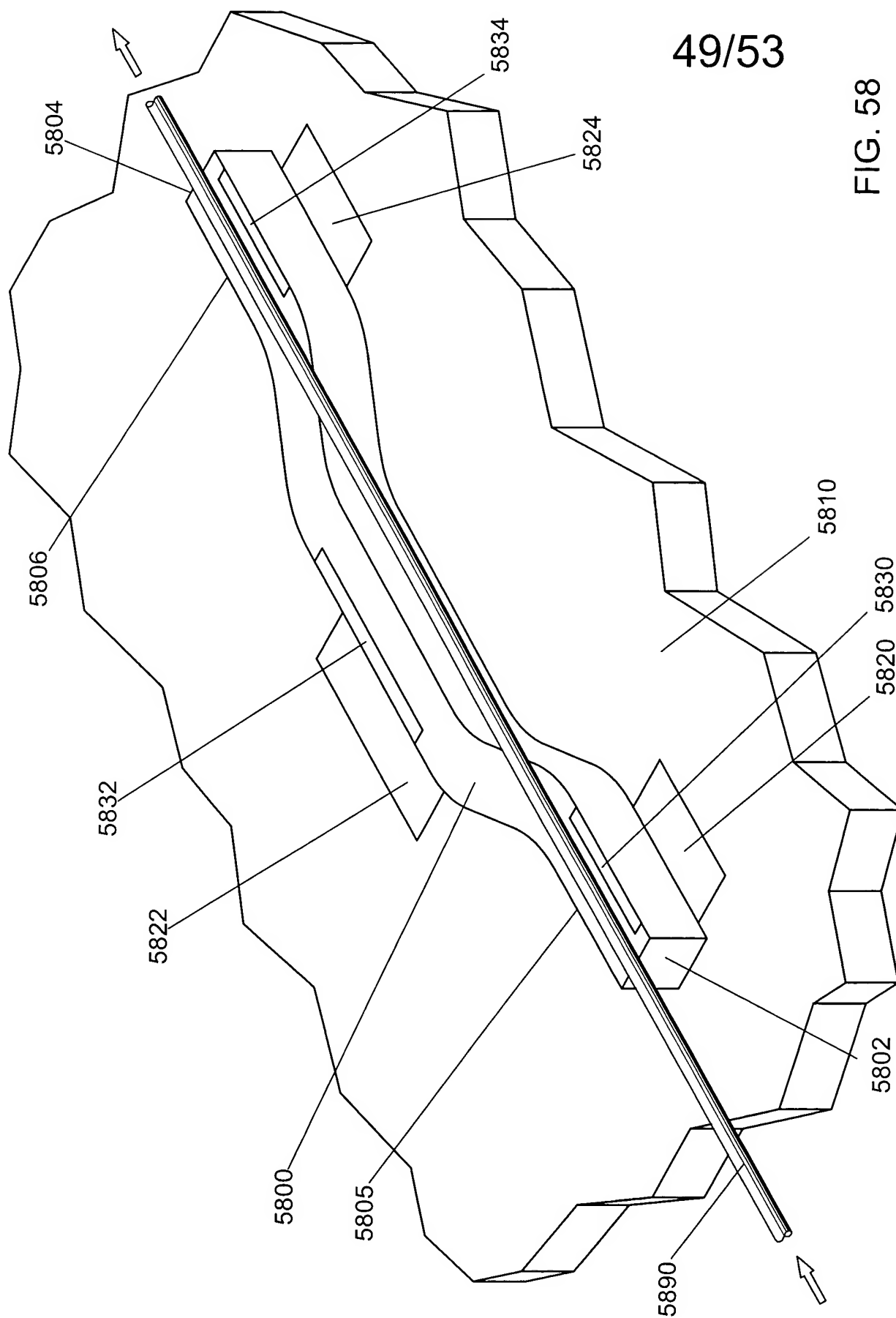
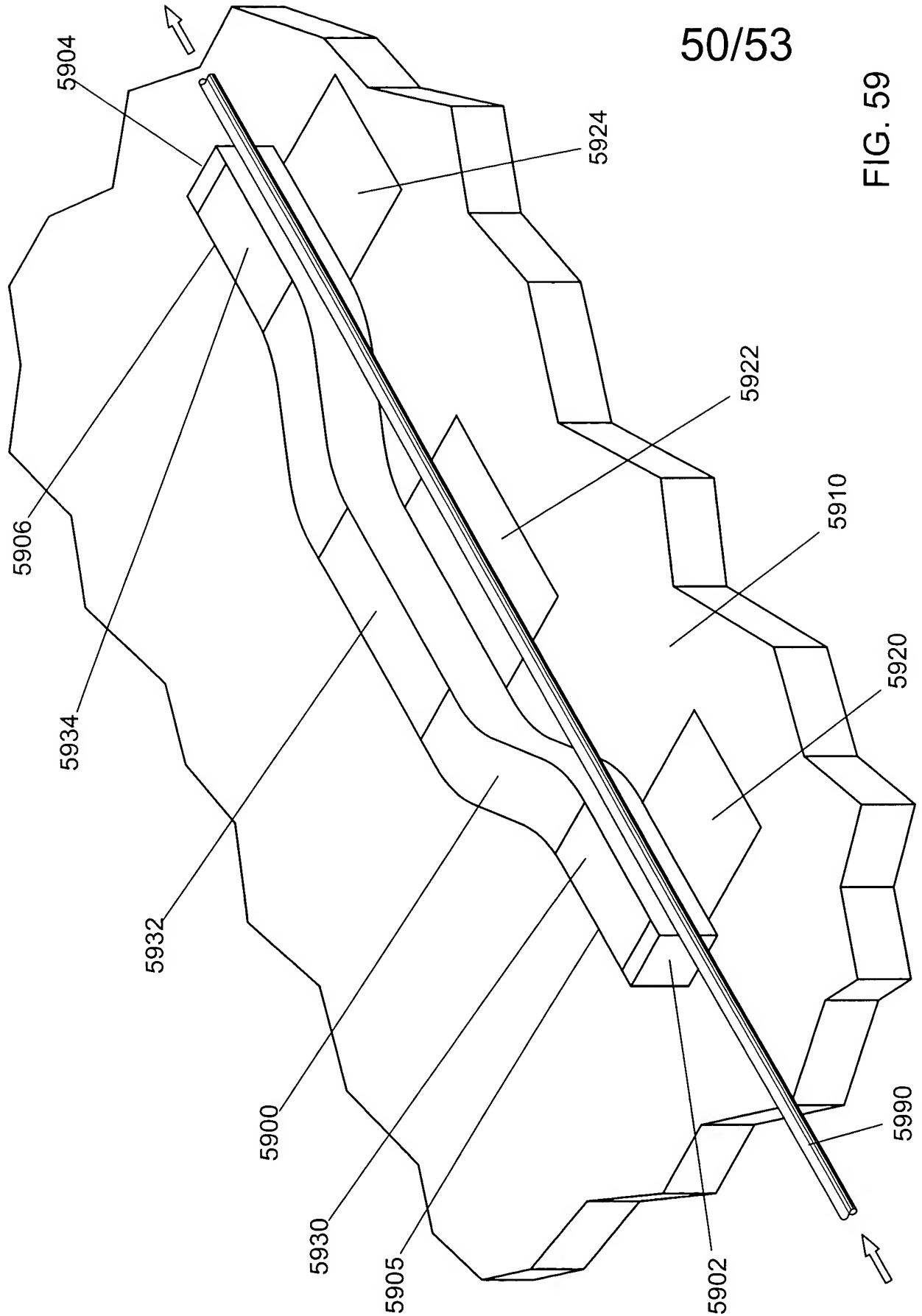


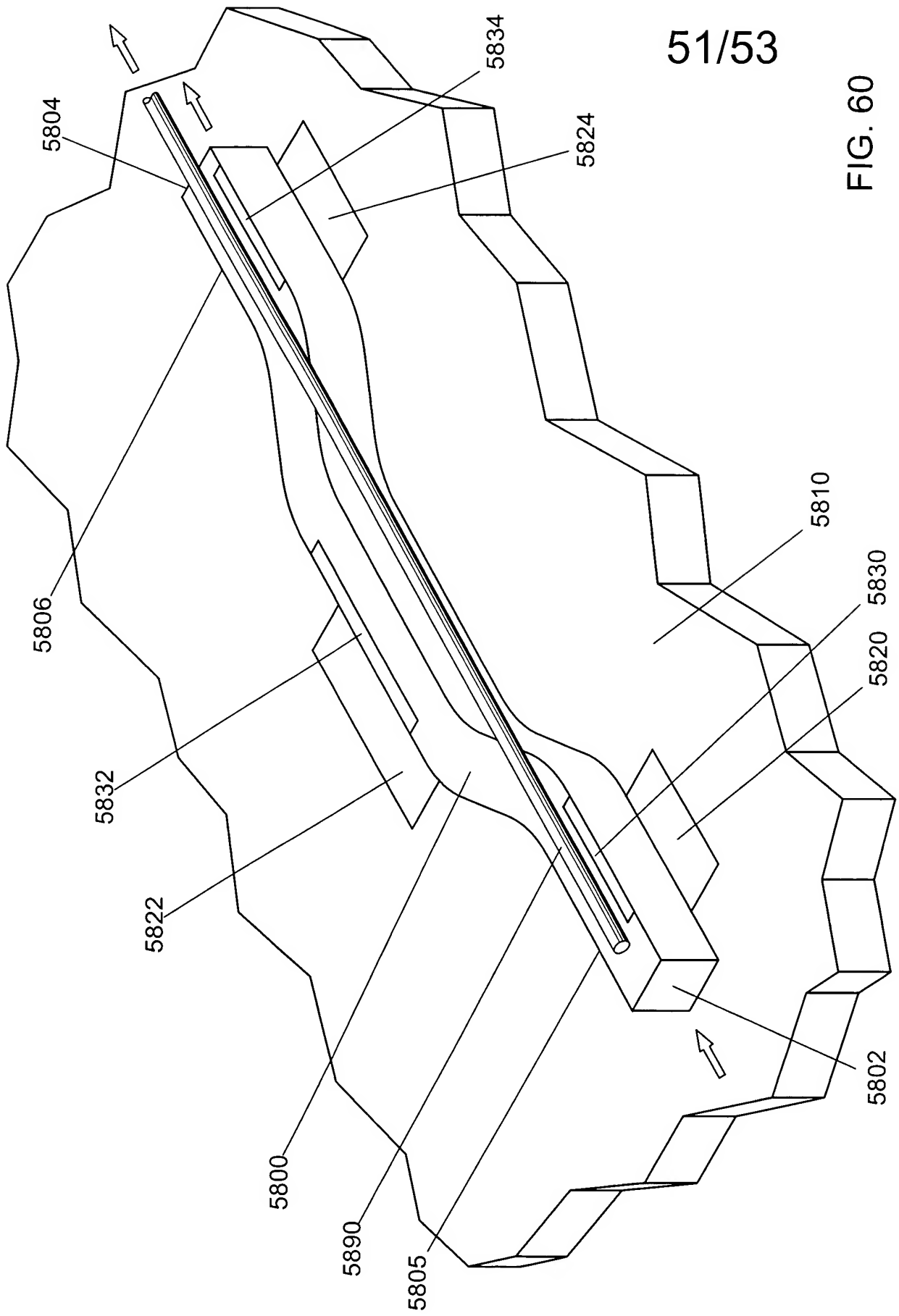
FIG. 58



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FIG. 59

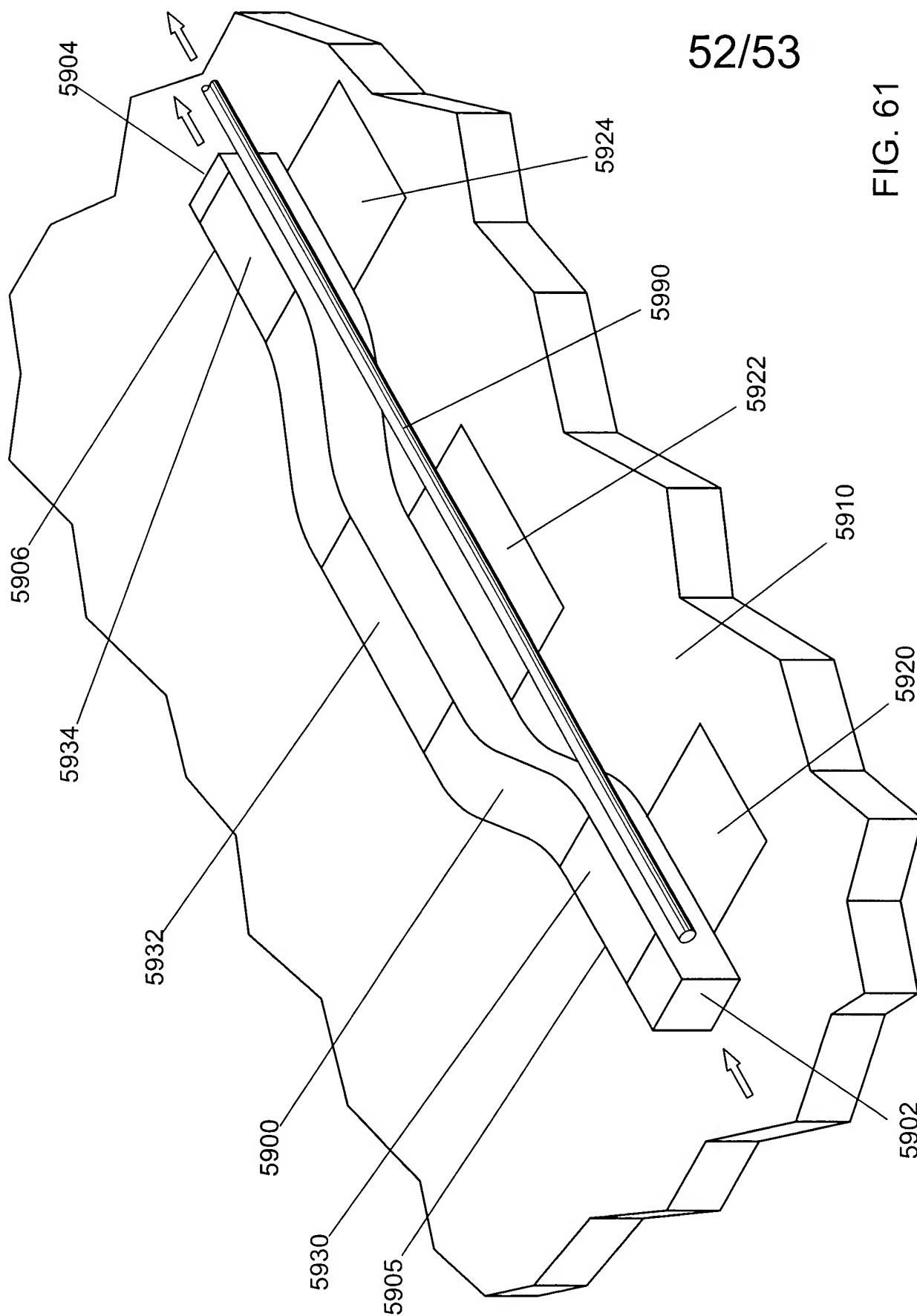
FIG. 60 is a perspective view of the device 5100 in a folded state, showing the device 5100 in a folded state, with the device 5100 in a folded state, and the device 5100 in a folded state.



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FIG. 60

FIG. 61



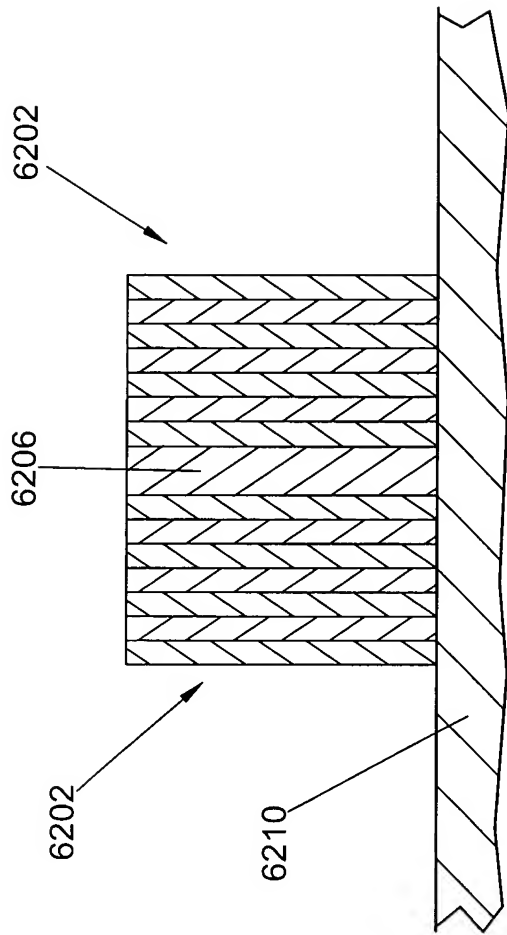


FIG. 62A

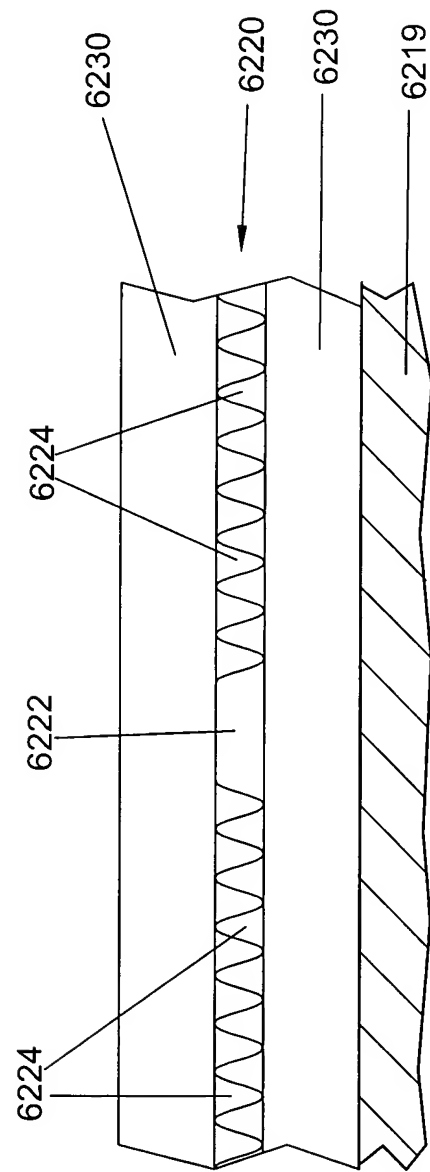


FIG. 62B